

User Manual

Rev070225

MIC-770 V2

**Compact Fanless System with
10th Gen Intel® Xeon®/Core™ i
CPU Socket (LGA 1200)**



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We want you to get the maximum performance from your products. So if you run into technical difficulties, we are here to help. For the most frequently asked questions, you can easily find answers in your product documentation. These answers are normally a lot more detailed than the ones we can give over the phone.

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In addition, free technical support is available from Advantech engineers every business day. We are always ready to give advice on application requirements or specific information on the installation and operation of any of our products.

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Product Warranty (2 years)

Advantech warrants the original purchaser that each of its products will be free from defects in materials and workmanship for two years from the date of purchase.

This warranty does not apply to any products that have been repaired or altered by persons other than repair personnel authorized by Advantech, or products that have been subject to misuse, abuse, accident, or improper installation. Advantech assumes no liability under the terms of this warranty as a consequence of such events.

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1. Collect all the information about the problem encountered. (For example, CPU speed, Advantech products used, other hardware and software used, etc.) Note anything abnormal and list any onscreen messages displayed when the problem occurs.
2. Call your dealer and describe the problem. Please have your manual, product, and any helpful information readily available.
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5. Write the RMA number clearly on the outside of the package and ship the package prepaid to your dealer.

Declaration of Conformity

FCC

This device complies with the requirements in part 15 of the FCC rules:

Operation is subject to the following two conditions:

- This device may not cause harmful interference.
- This device must accept any interference received, including interference that may cause undesired operation.

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this device in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his/her own expense. The user is advised that any equipment changes or modifications not expressly approved by the party responsible for compliance would void the compliance to FCC regulations and therefore, the user's authority to operate the equipment.

Caution! *There is a danger of a new battery exploding if it is incorrectly installed. Do not attempt to recharge, force open, or heat the battery. Replace the battery only with the same or equivalent type recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions.*



Technical Support and Assistance

1. Visit the Advantech website at <http://support.advantech.com> to obtain the latest product information.
2. Contact your distributor, sales representative, or Advantech's customer service center for technical support if you need additional assistance. Please have the following information ready before calling:
 - Product name and serial number
 - Description of your peripheral attachments
 - Description of your software (operating system, version, application software, etc.)
 - A complete description of the problem
 - The exact wording of any error messages

Warnings, Cautions, and Notes

Warning! Warnings indicate conditions that if not observed can cause personal injury!



Caution! Cautions are included to help prevent hardware damage and data losses. For example,



“Batteries are at risk of exploding if incorrectly installed. Do not attempt to recharge, force open, or heat the battery. Replace the battery only with the same or equivalent type as recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions.”

Note! Notes provide additional optional information.



Safety Instructions

1. Read these safety instructions carefully.
2. Retain this user manual for future reference.
3. Disconnect the equipment from all power outlets before cleaning. Use only a damp cloth for cleaning. Do not use liquid or spray detergents.
4. For pluggable equipment, the power outlet socket must be located near the equipment and easily accessible.
5. Protect the equipment from humidity.
6. Place the equipment on a reliable surface during installation. Dropping or letting the equipment fall may cause damage.
7. The openings on the enclosure are for air convection. Protect the equipment from overheating. Do not cover the openings.
8. Ensure that the voltage of the power source is correct before connecting the equipment to a power outlet.
9. Position the power cord away from high-traffic areas. Do not place anything over the power cord.
10. All cautions and warnings on the equipment should be noted.
11. If the equipment is not used for a long time, disconnect it from the power source to avoid damage from transient overvoltage.
12. Never pour liquid into an opening. This may cause fire or electrical shock.
13. Never open the equipment. For safety reasons, the equipment should be opened only by qualified service personnel.
14. If any of the following occurs, have the equipment checked by service personnel:
 - The power cord or plug is damaged.
 - Liquid has penetrated the equipment.
 - The equipment has been exposed to moisture.
 - The equipment is malfunctioning, or does not operate according to the user manual.
 - The equipment has been dropped and damaged.
 - The equipment shows obvious signs of breakage.
15. Do not leave the equipment in an environment with a storage temperature of below -40 °C (-40 °F) or above 85 °C (185 °F) as this may damage the components. The equipment should be kept in a controlled environment.
16. CAUTION: Batteries are at risk of exploding if incorrectly replaced. Replace only with the same or equivalent type as recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions.
17. In accordance with the IEC 704-1:1982 specifications, the sound pressure level at the operator's position should not exceed 70 dB (A).
18. RESTRICTED ACCESS AREA: The equipment should only be installed in a Restricted Access Area.

DISCLAIMER: These instructions are provided according to IEC 704-1 standards. Advantech disclaims all responsibility for the accuracy of any statements contained herein.

Safety Precautions

Warning! Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.



Caution! Always ground yourself to remove any static charge before touching the motherboard. Modern electronic devices are very sensitive to static electric discharges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components on a static-dissipative surface or in a static-shielded bag when they are not in the chassis.



Caution! The computer is provided with a battery-powered real-time clock circuit. There is a danger of explosion if the battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions.



Caution! There is a danger of a new battery exploding if it is incorrectly installed. Do not attempt to recharge, force open, or heat the battery. Replace the battery only with the same or equivalent type recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions.



Packing List

Before system installation, check that the items listed below are included and in good condition. If any item does not accord with the list, contact your dealer immediately.

■	MIC-770 V2 bare-bone system x 1	
■	Startup Manual EN/CN/TC for MIC-770 V2 1st Ed	P/N: 2041077030
■	4-pin Phoenix connector	P/N: 1652003234
■	Mounting bracket x 2	P/N: 1960070543T005
■	SATA cable x 1	P/N: 1700013095-01
■	SATA power cable x 1	P/N: 1700024372-01
■	CPU thermal grease x 1	P/N: 2170000093-01
■	RAM thermal pad x 2	P/N: 1990019498N000

Ordering Information

Part Number	VGA	HDMI	2.5" HDD /SSD	mSATA	USB 3.2 (Gen2)	USB 3.2 (Gen1)	USB 2.0	GbE	COM	PCIe/PCI Exp	Power
MIC-770W-20A1	1	1	1	1	2	6	0	2	2, up to 6 (Optional)	i-module (Optional)	9~36V _{DC}
MIC-770H-20A1	1	1	1	1	0	4	4	2	2, up to 6 (Optional)	i-module (Optional)	9~36V _{DC}

Optional i-Module

Module Number	Description
4-slot expansion module	
MIC-75M13-00A2	1 x PCIe x16 + 3 x PCI slot i-Module
MIC-75M40-00A2**	1 x PCIe x8 + 3 x PCIe x4 slot i-Module
2-slot expansion module	
MIC-75M20-00C1	1 x PCIe x16 + 1 x PCIe x4 slot i-Module
MIC-75M20-01A2	2 x PCIe x8 slot i-Module
MIC-75M11-00A2	1 x PCIe x16 + 1 x PCI slot i-Module
1-slot expansion module	
MIC-75M10-00A2	1 x PCIe x16 slot
Featured expansion module	
MIC-75S00-00A1	2 x removable storage
MIC-75S20-00A2	1 x PCIe x16 + 1 x PCIe x4 slot + 2 x 2.5" swappable HDD/SSD
MIC-75G20-10B1	1 x PCIe x16 (double deck) + 1 x PCIe x4 + 2 x 2.5" hot-swappable HDD/SSD GPU slot i-Module
MIC-75G30-00B1**	2 x PCIe x8 + 1 x PCIe x4 slot + 2 x 2.5" swappable HDD/SSD
MIC-75GF10-00A1	MIC-75GF10 MXM i-Module, 1 x PCIe x16 + 1 x PCIe x4
98R17520301	2 x 3.5" HDD kit

* Please refer to the i-Module datasheet for more details.

** W480E SKU only.

Optional Accessories

Part Number	Description
96PSA-A230W24P4-3*	ADP A/D 100-240V 230W 24V C14 TERMINAL BLOCK 4P
96PSA-A150W19P4-4	ADP A/D 100-240V 150W 19V C14 TERMINAL BLOCK 4P
1702002600	Power cord (USA) UL/CSA, 3-pin, 10A, 125V, 1.83M, 180 D
1700022940-01	Power cord PSE, 3-pin, 7A, 125V, 3M, DAC-ST01
1702002605	Power cord (EU), 3-pin, 10A, 250V 1.83M, 90D
96PSD-A240W24-MN	A/D 100-240V 240W 24V NDR DIN RAIL
1700031170-01	DC-DC power cord, A cable TEM*4/TEM*4 UL2464 18AWG 150cm
1700029720-01	AC-DC power cord (US), M cable AC CONN 3P 183cm
1700030520-01	AC-DC power cord (CN), M cable conn 3P CCC 10A 250V 150cm
1700034561-01	AC-DC power cord (EU), M cable Type E 3P/TEMx3 80cm
AIIS-DIO32-00A1E**	AIIS GPIO module (32-bit)
PCA-TPM-00B1E	TPM 2.0 module
98R17500001	MIC DVI FIO
98R17500101	MIC HDMI/Remote power on/off FIO
98R17500301	MIC HDMI kit FIO
98R17500401	MIC Remote power on/off FIO
98R17500601	MIC COM port kit FIO
98R17500701	MIC Remote power on/off kit for SFIO
98R17500801	MIC Reset/Remote power on/off/5V _{DC} kit FIO
98R17500901	MIC GPIO kit
98R17501001	MIC Display Port kit
98R17501101	MIC COM and HDMI kit
98910770301	MIC NVMe + 4 LAN Advanced FIO
98910770401	MIC NVMe Advanced FIO
98910770501	MIC 4 PoE Advanced FIO
98R1752010E	2nd 2.5" HDD/SSD kit (used in 2-slot i-Module)
98R1752020E	MIC Dual SSD kit
98R17500201	MIC DIN Rail Mounting kit (90/180)
98R17500210	MIC DIN Rail Mounting kit (180)
98R17500501	MIC Wall Mounting kit
i-Door Module (MOS series module)	Supports i-Door module (MOS series), except PoE Please refer to the Advantech website below or search for "iDoor Module Mini PCIe Expansion Kit". http://www.advantech.com.tw/products/idoor-module-mini-pcie-expansion-kit/sub_bc858a7f-a52b-441b-a59c-f511289f98bc
i-Door Module (PCM series module)	Supports i-Door module (PCM series) except PoE Please refer to the Advantech website below: http://www.advantech.com/products/idoor-technology-mini-pcie-expansion-kit/sub_efdb96af-a8f7-4cde-9592-dbf5c9794d16 Note: A bracket is required to fix the PCM series i-door module in place. Please refer to P/N: 1960065854N001 i Door_bracket

i-module Optional Accessories

Part Number	Description
98R1751300E	1 x 8cm FAN module (for MIC-75M13/75M40/75S20)
98R1752000E	2 x 4cm FAN module (for MIC-75M20/MIC-75M11)

Please refer to the Flex I/O datasheet for more details.

* Please use a 230W adapter when the i-Module is added.

** AllS-DIO32 requires a DB37 bracket (P/N: 1960068787N002).

*** R480E SKU only.

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Chapter 1

General Introduction

This chapter gives background information on MIC-770 V2.

1.1 Introduction

MIC-770 V2 is a compact, fanless system incorporating the latest generation Intel® 14nm platform with the new PCH W480E/H420E on a proprietary form factor motherboard. The MIC-770 V2 system design concept is focused on the “expansion slot module” so different applications can integrate the MIC-770 V2 system as a complete industrial computer.

MIC-770 V2 can also serve as an independent, fanless, compact embedded box computer and accepts a wide range of DC power input. The rugged aluminum case not only provides a great thermal solution, but also resists high EMI/shock/vibration. MIC-770 V2 is equipped with a 10th Gen Intel® Xeon® / Core™ i CPU featuring up to 10 cores, making it highly suitable for embedded and industrial PC applications requiring high processor performance within limited physical spaces. It features powerful I/O interfaces, including Ethernet, USB 3.0, serial ports, and two Mini PCIe ports.

MIC-770 V2 (W SKU) also adopts the Advantech WISE-iBMC out-of-band management solution for remote power control via NIC port or LAN2 when the OS is abnormal.

With all these excellent features and outstanding performance, the MIC-770 V2 is the ideal platform for today's industrial applications.

1.2 Product Features

1.2.1 General

- **CPU:** 10th Gen Intel® Xeon® / Core™ i CPU socket (LGA1200) (Xeon® is supported by the W480E SKU only)
- **PCH:** Intel® W480E/H420E
- **System Memory:** Dual-channel DDR4 2666/2933 MHz (ECC SO-DIMM) (W480E SKU only), up to 64GB
- **Storage Devices:** Supports 1 drive bay space for SATA 2.5" HDD/SSD (Up to 2x with the optional kit)
- **mSATA:** Supports 1 x mSATA by Mini PCIe slot
- **Graphics:** VGA + HDMI
- **Ethernet Ports:** 2 x RJ-45
- **Watchdog Timer:** Single chip watchdog 255-level interval timer, setup by software
- **I/O Interface:** 2 x RS-232/422/485 supports auto flow control; 4 x RS-232 via optional cable
- **USB:**
 - W480E: 2 x USB 3.2 (Gen.2), 6 x USB 3.2 (Gen.1)
 - H420E: 4 x USB 3.2 (Gen.1) and 4 x USB 2.0
- **Audio:** High Definition Audio (HD), Line-out, Mic-in
- **Expansion Interface:**
 - W480E: 1 x Mini PCIe (via Nano-SIM), 1 x Mini PCIe / mSATA
 - H420E: 1 x Mini PCIe (via Nano-SIM), 1 x mSATA

1.2.2 Display

- **Chipset:**
 - W480E: Intel® HD Graphics 630, supports DirectX 12
 - H420E: Intel® HD Graphics 610, supports DirectX 12
- **Resolution:**
 - VGA: Supports up to 2048 x 1152 @ 60 Hz
 - HDMI: Supports up to 3840 x 2160 @ 30Hz

1.2.3 Ethernet

- **Controller:**
 - W480E LAN1: Intel® I219LM, LAN2: Intel® i210IT (supports iBMC)
 - H420E LAN1: Intel® I219V, LAN2: Intel® i210IT
- **Speed:** 10/100/1000 Mbps
- **Interface:** 2 x RJ-45
- **Standard:** Compliant with IEEE 802.3, IEEE 802.3u, IEEE 802.ab.

1.2.4 SUSI API

MIC-770 V2 supports SUSI API which helps customers develop their own remote management programs under Windows 10.

1.2.5 WISE-iBMC Out-of-Band Power Management

- **The MIC-770 V2 W480E SKU is equipped with the WISE-iBMC solution, which provides out-of-band management for remote power-on/power-off/reset/force-shutdown via LAN2 if abnormal conditions are selected.**

Note!



The WISE-iBMC power control function is operated on Advantech WISE-DeviceOn software. Before installation and setup of the WISE-Agent program, changing the device IP address to the same as your network IP via a utility tool is necessary. Please refer to the product support website to download the iBMC utility, DeviceOn software, and SOP / user manual.

1.3 Chipset

1.3.1 Functional Specifications

1.3.1.1 Processor

Processor	10th Gen Intel® Xeon® / Core™ i CPU socket (LGA1200) (Xeon® is supported by the W480E SKU only)
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1.3.1.2 Chipset

PCH	■ Intel® W480E/H420E chipset
Memory	■ Supports DDR4 2666/2933 MHz ECC RAM (W480E SKU only) (without ECC) ■ SO-DIMM Socket: – 260-pin SO-DIMM socket*2 (up to 32GB per socket)
Chipset Integrated Intel® HD Graphics	■ Intel® HD Graphics 630/610 ■ Supports DirectX 12 ■ Supports OpenGL 4.4 ■ Supports Intel® Quick Sync Video ■ I/O interface – VGA: Supports resolutions up to 2048 x 1152 @ 60 Hz (VGA connector: On-board D-SUB 15P) – HDMI: Supports resolutions up to 3840 x 2160 @ 30Hz (HDMI Connector: On-board HDMI)
SATA Interface	■ 3 x SATA 3.0 (W SKU), 2 x SATA 3.0 (H SKU) ■ Legacy IED (Including IRQ)/Native AHCI appearance to OS ■ Partial/slumber power management modes with wake ■ Capable of 6 Gbit/s transfer rate
USB Interface	■ W480E: 2 x USB 3.2 (Gen.2), 6 x USB 3.2 (Gen.1) ■ H420E: 4 x USB 3.2 (Gen.1) and 4 x USB 2.0 ■ Supports high-speed, full-speed, and low-speed ■ Supports legacy keyboard/mouse software
Power Management	■ Supports ACPI 5.0 ■ ACPI Power Management Logic supported ■ Power Connector: plug-in block 4Px1
BIOS	■ AMI 256Mb (W SKU)/128Mb (H SKU) Flash BIOS via SPI

Note! Legacy platform not supported.



1.3.1.3 Others

Serial Ports	Nuvoton NCT 6106D supported <ul style="list-style-type: none"> ■ Up to 6 serial ports by Nuvoton NCT6106D supported ■ High-speed NS16C550A compatible UARTs with data rates up to 115200 ■ Supports IRQ sharing among serial ports ■ COM1/2: Supports RS-232/422/485 and mode setting by BIOS and supports auto-flow control ■ COM 3~6: Support for RS-232 via optional cable Serial port connector: D-SUB CON.9P
LAN	W480E LAN1: Intel® I219LM (PHY), LAN2: Intel® i210IT I210IT (NIC) (Supports iBMC) H420E LAN1: Intel® I219V (PHY), LAN2: Intel® i210IT I210IT (NIC) <ul style="list-style-type: none"> ■ Compliant with IEEE 802.3, IEEE 802.3u, IEEE 802.ab. ■ Supports 10/100/1000 Mbps ■ Supports Wake-on-LAN
Audio	Audio Codec: Realtek ALC888: <ul style="list-style-type: none"> ■ Compliant with HD Audio specifications ■ Supports 16/20/24-bit DAC and 16/20/24-bit ADC resolution ■ Supports: Line-out, Mic-in ■ DAC supports 16/20/24-bit PCM format, multiple stereo recordings
Industrial Features	Watchdog timer: <ul style="list-style-type: none"> ■ Can generate system reset or NC. The watchdog timer is programmable, with each unit equal to one second (255 levels).
Battery Backup	<ul style="list-style-type: none"> ■ BR2032 3V / 190 mAh

1.4 Mechanical Specifications

1.4.1 Dimensions

UNIT : mm

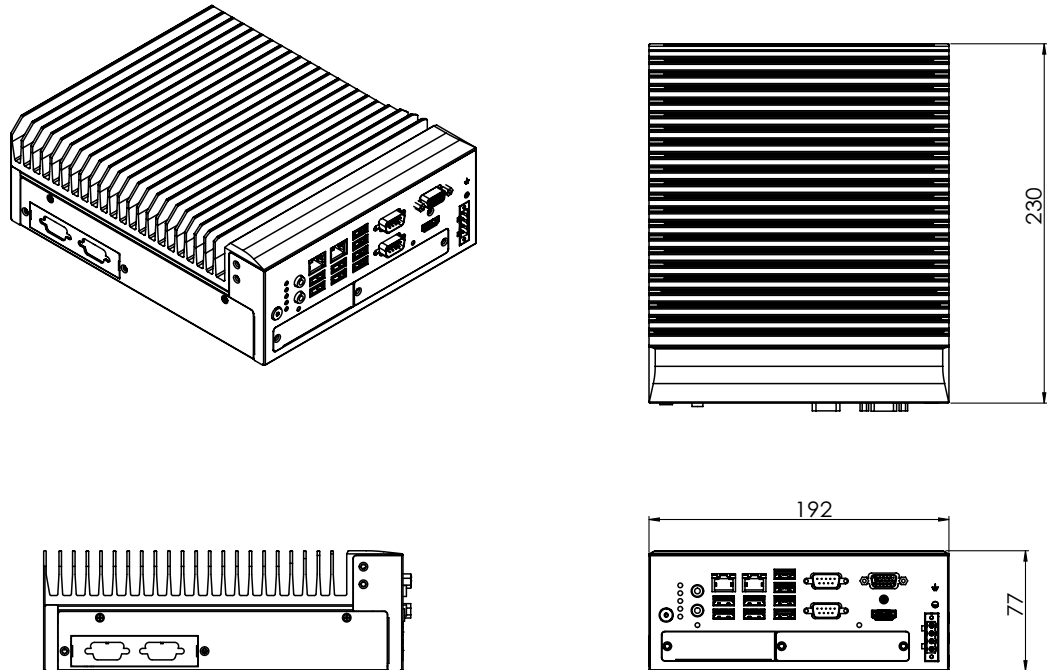


Figure 1.1 MIC-770 V2 Mechanical Dimension Drawing

1.4.2 Weight

2.8 kg (6.17 lb)

1.5 Power Requirements

1.5.1 System Power

- Minimum power input: DC12V (-25%) -30V (+20%), Absolute Maximum Ratings Voltage is 9V - 36V

1.5.2 RTC Battery

- BR2032 3 V/190 mAh

1.6 Environmental Specifications

1.6.1 Operating Temperature Range

- -10 ~ 50 °C (14 ~ 122 °F) (65W CPU) & -10 ~ 60 °C (14 ~ 140 °F) (35W CPU) with 0.7 m/sec airflow: with 1 x industrial wide-temp SSD

1.6.2 System Safety Certification Test Temperature Range

- 0 ~ 40 °C with (32 ~ 104 °F) 2.5" SSD

1.6.3 Relative Humidity

- 95% @ 40°C (non-condensing)

1.6.4 Storage Temperature

- -40 ~ 85 °C (-40 ~ 185 °F)

1.6.5 Vibration During Operation

- When the system is equipped with SSD only: 3 Grms, IEC 60068-2-64, random, 5 ~ 500 Hz, 1 hr/axis, x,y,z 3 axes.
- When system is equipped with a 2.5" HDD: 1 Grms, IEC 60068-2-64, random, 5 ~ 500 Hz, 1 hr/axis, x,y,z 3 axes.

1.6.6 Shock During Operation

- When system is equipped with SSD only: 20 G, IEC 60068-2-27, half-sine, 11 ms duration.

1.6.7 Safety

- UL/CB, CCC, BSMI

1.6.8 EMC

- CE, FCC, CCC, BSMI

Chapter 2

H/W Installation

This chapter introduces external IO and the installation of the MIC-770 V2 hardware.

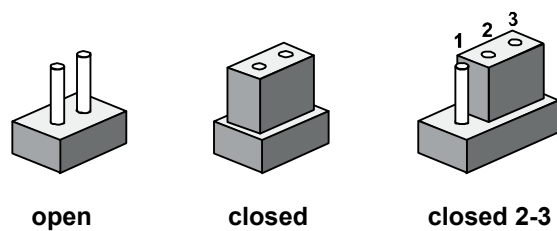
2.1 Introduction

The following sections show the internal jumper settings and the external connectors and pin assignments.

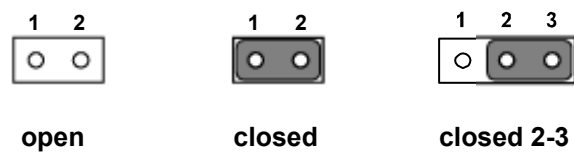
2.2 Jumper & Slide Switch

2.2.1 Jumper Description

You may configure MIC-770 V2 to match the needs of your application by setting jumpers. A jumper is a metal bridge used to close an electric circuit. It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To close a jumper, you connect the pins with a clip. To open a jumper, you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case you would connect either pins 1 and 2, or 2 and 3.



The jumper settings are schematically depicted in this manual as follows.



A pair of needle-nose pliers may be helpful when working with jumpers. If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes. Generally, you simply need a standard cable to make most connections.

2.2.2 Jumper List

Table 2.1: Jumper List

Label	Function
JCMOS1	Clear CMOS
PSON1	System AT/ATX mode option
JME1	ME jumper mode option
JWDT1_JOBS1	Watchdog mode option

2.2.2.1 Clear CMOS

The MIC-770 V2 single-board computer contains a jumper that can erase CMOS data and reset the system BIOS information. Normally, this jumper should be set with pins 1-2 closed. If you want to reset the CMOS data, set CMOS1 to 2-3 closed for just a few seconds, then move the jumper back to 1-2 closed. This procedure will reset the CMOS to its default setting.

CMOS1	Clear CMOS
Footprint	3x1 Pin
Setting	Function
(1-2)	Normal (default)
(2-3)	Clear CMOS

2.2.2.2 System AT/ATX Mode Function Option

MIC-770 V2 supports AT or ATX mode and the default is the ATX module. If you want to change to AT mode you can locate the AT/ATX mode jumper on the motherboard.

PSON1	System AT/ATX mode option
FootPrint	3x1 Pin
Setting	Function
(1-2)	AT module
(2-3)	ATX module

2.2.2.3 System ME Mode Function Option

MIC-770 V2 supports ME Enable & Disable and the default is "Disable". If you want to change the ME mode, you can set the ME mode jumper on the motherboard.

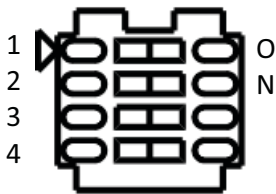
JME1	System ME mode option
Footprint	3x1 Pin
Setting	Function
(1-2)	ME Enable (Default)
(2-3)	ME Disable

2.2.2.4 System Watchdog Mode Function Option

MIC-770 V2 single-board computer contains a jumper that can set watchdog mode.

JWDT1_JOBS1	Watchdog mode function option
Footprint	5x1 pin
Setting	Function
(2-3)	Watchdog
(4-5)	ERR_BEEP

2.2.2.5 USB Standby Power & VGA Always-On Setting (DIP Switch)



DIP Switch (SW5)

Switch	State	Setting
SW5-1	1 (default)	USB3C1 does not provide standby charging
	On	USB3C1 provides standby charging
SW5-2	2 (default)	USB3C2 does not provide standby charging
	On	USB3C2 provides standby charging
SW5-3	3 (default)	USB3C3 does not provide standby charging
	On	USB3C3 provides standby charging
SW5-4	4 (default)	VGA is always on
	On	VGA on when cable detected

2.3 Connectors

2.3.1 MIC-770 V2 External I/O Connectors

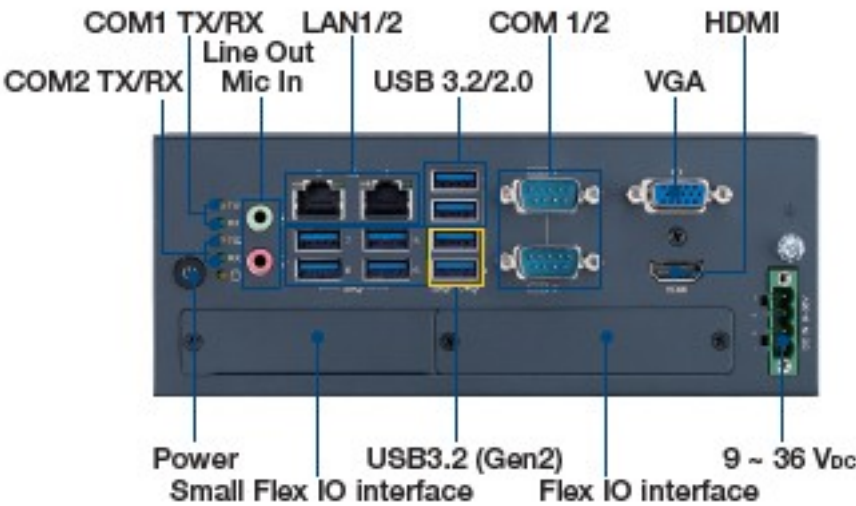


Figure 2.1 MIC-770 V2 Front View

2.3.1.1 COM Connector

MIC-770 V2 provides two 9-pin D-sub connectors and offers RS-232/422/485 serial communication interface ports. The default setting is ES-232, but this can be modified in the BIOS settings. You can find detailed instructions in Chapter 3.

Note! Expandable to four additional RS-232 via optional cables.



Table 2.2: COM Connector Pin Assignments

	RS-232	RS-422	RS-485
Pin	Signal Name	Signal Name	Signal Name
1	DCD	Tx-	DATA-
2	RxD	Tx+	DATA+
3	TxD	Rx+	NC
4	DTR	Rx-	NC
5	GND	GND	GND
6	DSR	NC	NC
7	RTS	NC	NC
8	CTS	NC	NC
9	RI	NC	NC

Note! NC represents “No Connection”.



2.3.1.2 Ethernet Connector (LAN)

MIC-770 V2 is equipped with two Ethernet controllers that are fully compliant with IEEE 802.3u 10/100/1000 Mbps CSMA/CD standards. LAN1 is equipped with Intel i219 and LAN2 is equipped with Intel i210. The Ethernet port provides a standard RJ-45 jack connector with LED indicators on the front side to show its Active/Link status and Speed status.

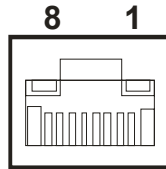


Figure 2.2 Ethernet Connector

Table 2.3: Ethernet Connector Pin Assignments

Pin	10/100/1000Base-T Signal Name
1	TX+
2	TX-
3	RX+
4	MDI2+
5	MDI2-
6	RX-
7	MDI3+
8	MDI3-

2.3.1.3 Audio Connector

MIC-770 V2 has two stereo audio ports with phone jack connectors, one Line_Out, and one Mic_In. The audio chip is controlled by ACL892, and is compliant with the AZALIA standard.



Figure 2.3 Audio Connector

Table 2.4: Audio Connector Pin Assignments

Pin	Audio Signal Name
1	Line_Out
2	Mic_In

2.3.1.4 USB 3.0 Connector

MIC-770 V2 provides USB 3.2/3.1 interface connectors, which provide complete Plug & Play and hot swapping for up to 127 external devices. The USB interface complies with USB XHCI, Rev. 3.0. Please refer to the table below for pin assignments.

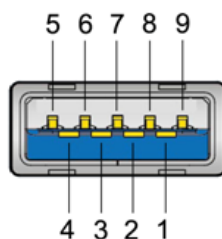


Figure 2.4 USB 3.2/3.1 Connector

Table 2.5: USB 3.0 Connector Pin Assignments

Pin 1	+5V
Pin 2	USB Data -
Pin 3	USB Data +
Pin 4	GND
Pin 5	SSRX-
Pin 6	SSRX+
Pin 7	GND
Pin 8	SSTX-
Pin 9	SSTX+

2.3.1.5 VGA Connector

The MIC-770 V2 provides a high-resolution VGA interface with a 15-pin D-sub connector to support a VGA CRT monitor. It supports display resolution of up to 2048 x 1152 @ 60 Hz.

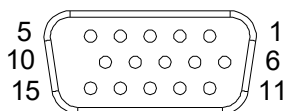


Figure 2.5 VGA Connector

Table 2.6: VGA Connector Pin Assignments

Pin	Signal Name	Pin	Signal Name
1	Red	2	Green
3	Blue	4	NC
5	GND	6	GND
7	GND	8	GND
9	+5V	10	GND
11	NC	12	DDC_DAT
13	H-SYNC	14	V-SYNC
15	DDC_CLK		

2.3.1.6 HDMI Connector

An integrated, 19-pin receptacle connector HDMI Type A Interface is provided. The HDMI link supports resolutions up to 2560 x 1600 @ 60Hz; 3840 x 2160 @ 30Hz.

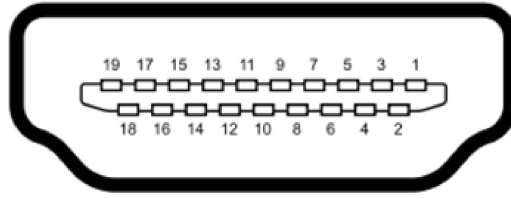


Figure 2.6 HDMI Receptacle Connector

Table 2.7: HDMI Connector Pin Assignments

Pin	Signal Name	Pin	Signal Name
1	TMDS Data 2+	2	TMDS Data 2 shield
3	TMDS Data 2-	4	TMDS Data 1+
5	TMDS Data 1 shield	6	TMDS Data 1-
7	TMDS Data 0+	8	TMDS Data 0 shield
9	TMDS Data 0-	10	TMDS clock+
11	TMDS clock shield	12	TMDS clock-
13	CEC	14	Reserved
15	SCL	16	SDA
17	DDC/CEC Ground	18	+5V
19	Hot Plug Detect		

2.3.1.7 Power Input Connector

MIC-770 V2 comes with a four-pin header as default that carries 9V_{DC} - 36V_{DC} external power input.

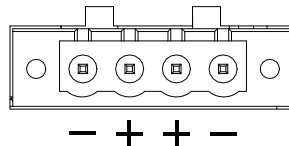


Figure 2.7 4-Pin Header

Table 2.8: Pin Assignments for Power Connector Pin Header

Pin	Signal Name
1	GND
2	+9 V _{DC} ~ 36 V _{DC}
3	+9 V _{DC} ~ 36 V _{DC}
4	GND

2.3.1.8 Power ON/OFF Button

MIC-770 V2 comes with a Power On/Off button with LED indicators on the front side to show its On status (Green LED) and Off/Suspend status (RED LED), that supports the dual function of Soft Power-On/Off (instant off or delay 4 seconds), and suspend.

Power button LED status:

System On: LED On

System Suspend: Fast flashes

System Off: Off

System Off in deep sleep: Off

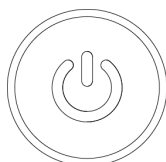


Figure 2.8 Power Button

2.3.1.9 LED Indicators

MIC-770 V2 provides COM1 & COM2 TX/RX LED for data transmission status monitoring.

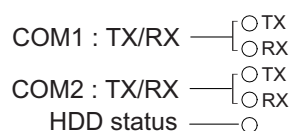
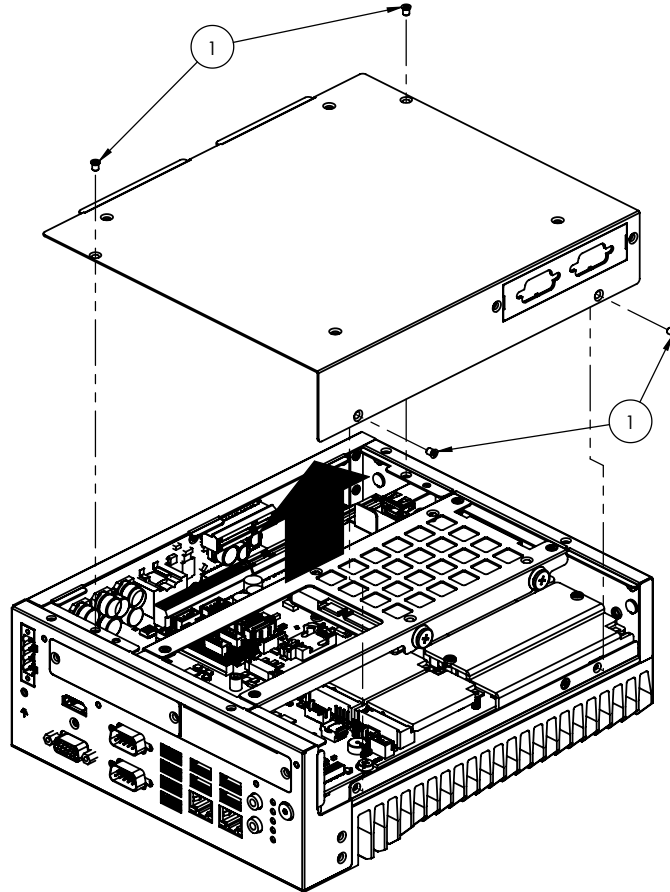


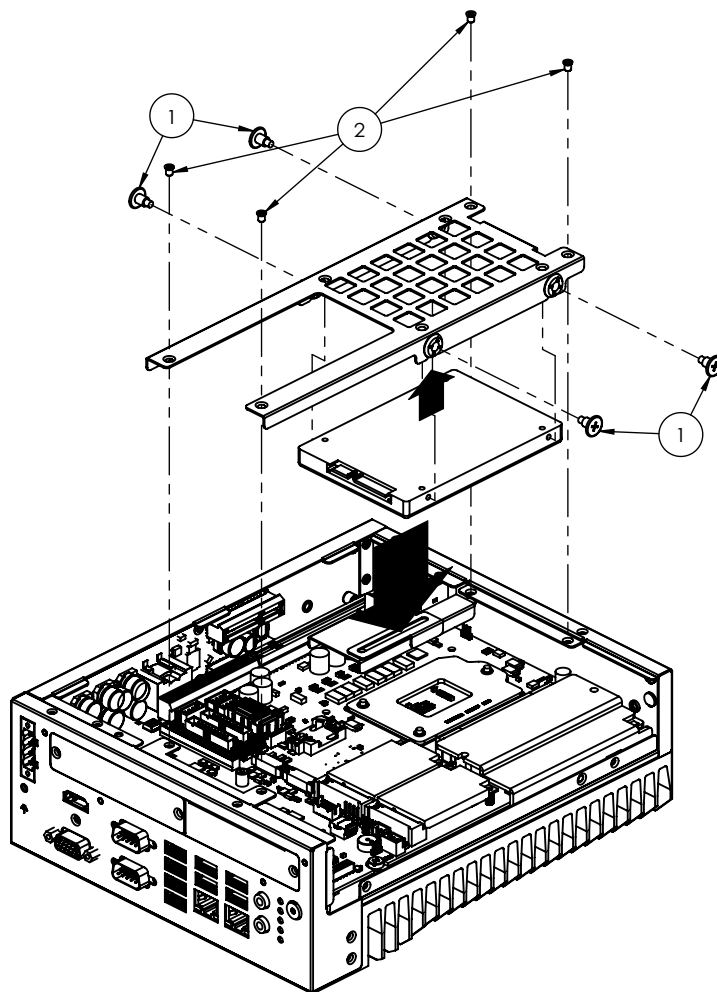
Figure 2.9 LED Indicators

2.4 Installation

2.4.1 HDD Installation

1. Undo 4 screws and remove the bottom cover.
2. Undo 4 screws to remove the HDD tray.





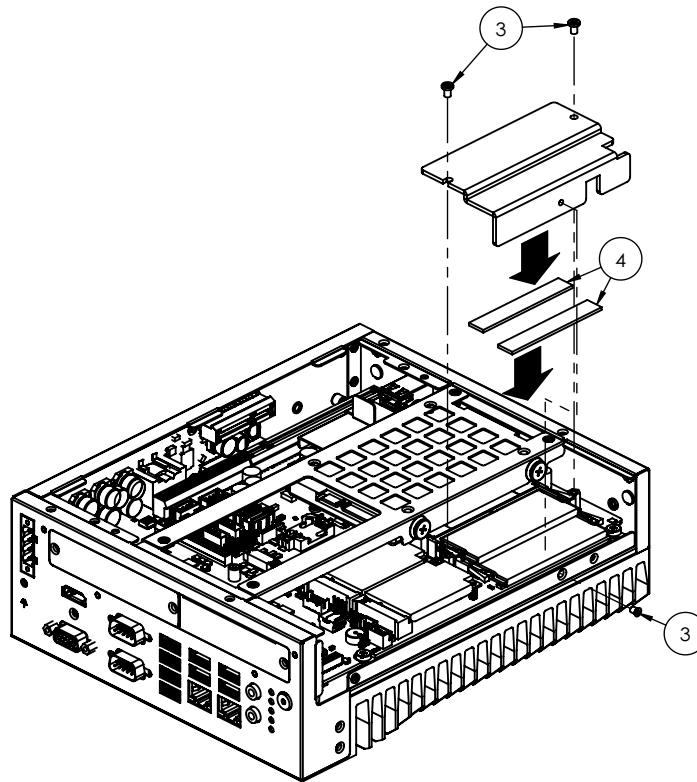
3. Secure the HDD with 4 x HDD screws (P/N:1930002235).
4. Connect the SATA cable / power cable and reassemble the HDD tray; secure with 4 screws.
5. Reassemble the bottom cover.

Note! Please refer to the *i-Module Manual* for *i-Module* assembly.



2.4.2 Memory Installation

1. Undo the 4 screws to remove the bottom cover.
2. Undo 4 screws to remove the HDD tray.



3. Undo 3 screws to remove the memory thermal cover.
4. Affix the thermal pad (P/N: 1990019498N000) on the memory, then place the memory back in position.

Note! The thermal pad and memory thermal cover must be completely covered and secured.

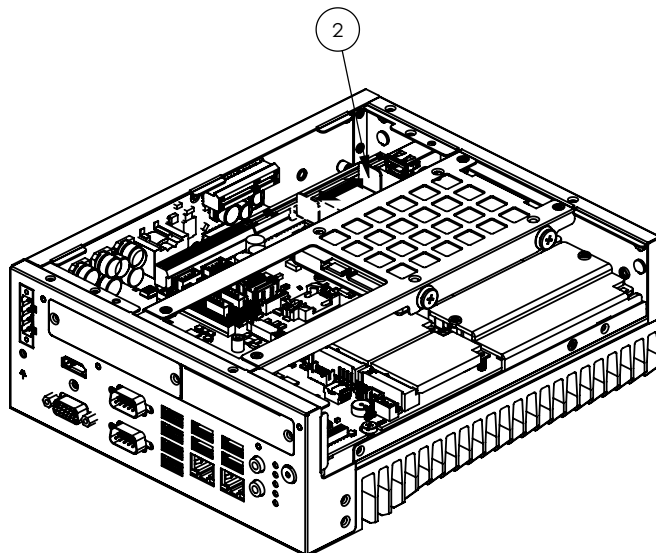


2.4.3 m-SATA / Mini PCIe Installation

- W480E: 1 x Mini PCIe (via Nano-SIM), 1 x Mini PCIe / mSATA
 - H420E: 1 x Mini PCIe (via Nano-SIM), 1 x mSATA
1. Undo 4 screws and remove the bottom cover.
 2. Install the module in Mini PCIe socket 1, or m-SATA in Mini PCIe socket 2 and secure with screws.
 3. Replace the bottom cover and secure with screws.

2.4.4 Internal USB 2.0 Installation (W SKU Only)

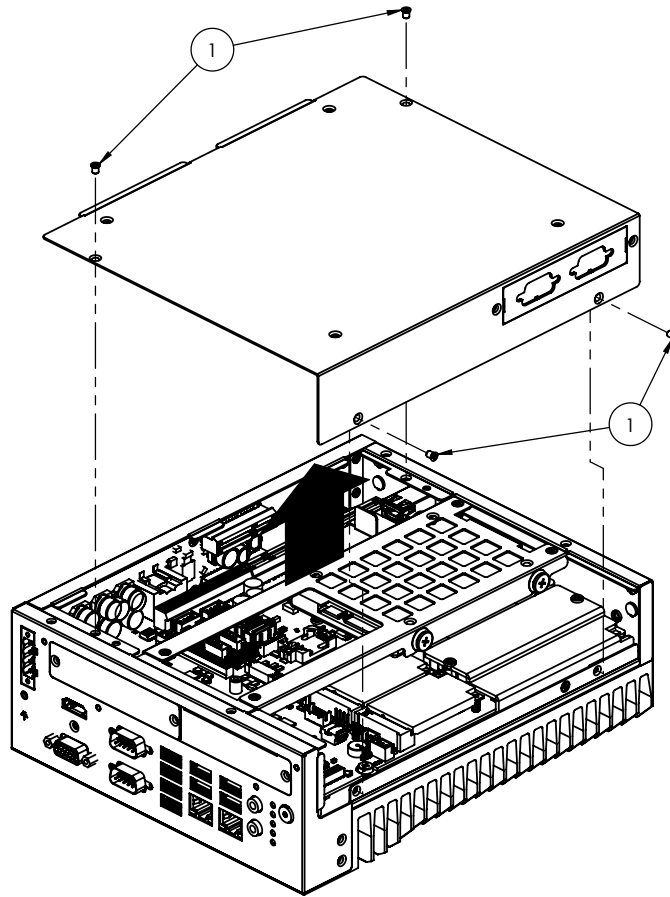
1. Undo 4 screws and remove the bottom cover.
2. Loosen the screws and adjust the bracket size in accordance with the USB dongle size.
3. Replace the bottom cover and secure with screws.



2.4.5 COM 3/4/5/6 Port Installation

MIC-770 V2 supports two standard RS-232/422/485 serial ports. If more serial ports are needed, MIC-770 V2 can expand to up to four serial ports by Flex I/O 98R17500601 (RS-232 only).

1. Replace the baffle plate for COM 3/4 and COM5/6.
2. Remove the HDD tray.
3. Assemble the DB9 connector for the baffle, and install the cable at the COM3/4 and COM5/6 connectors.
4. Secure the COM module in the system and replace the HDD tray/bottom cover.



2.4.6 Expansion Module Installation (Optional)

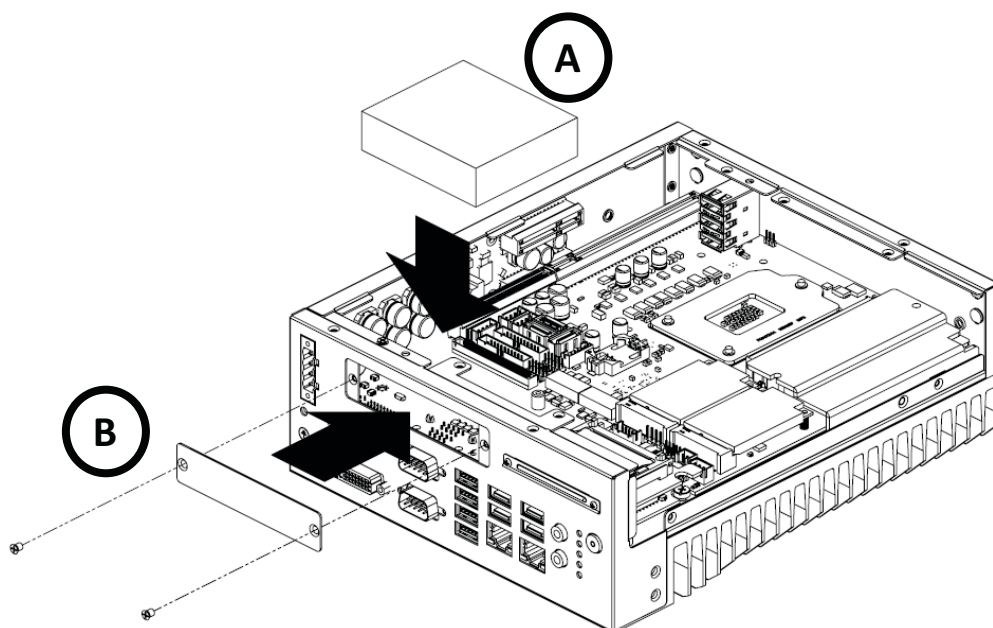
MIC-770 V2 supports the below optional modules for different applications.

A: Expansion modules:

1. 98R17500101 MIC HDMI/Remote power on/off FIO
2. 98R17500301 MIC HDMI kit FIO
3. 98R17500401 MIC Remote power on/off FIO
4. 98R17500601 MIC COM port kit FIO
5. 98R17500701 MIC Remote power on/off kit for SFIO
6. 98R17500801 MIC Reset/Remote power on/off/5V_{DC} kit FIO
7. 98R17500901 MIC GPIO kit FIO
8. 98R17501001 MIC DP kit FIO

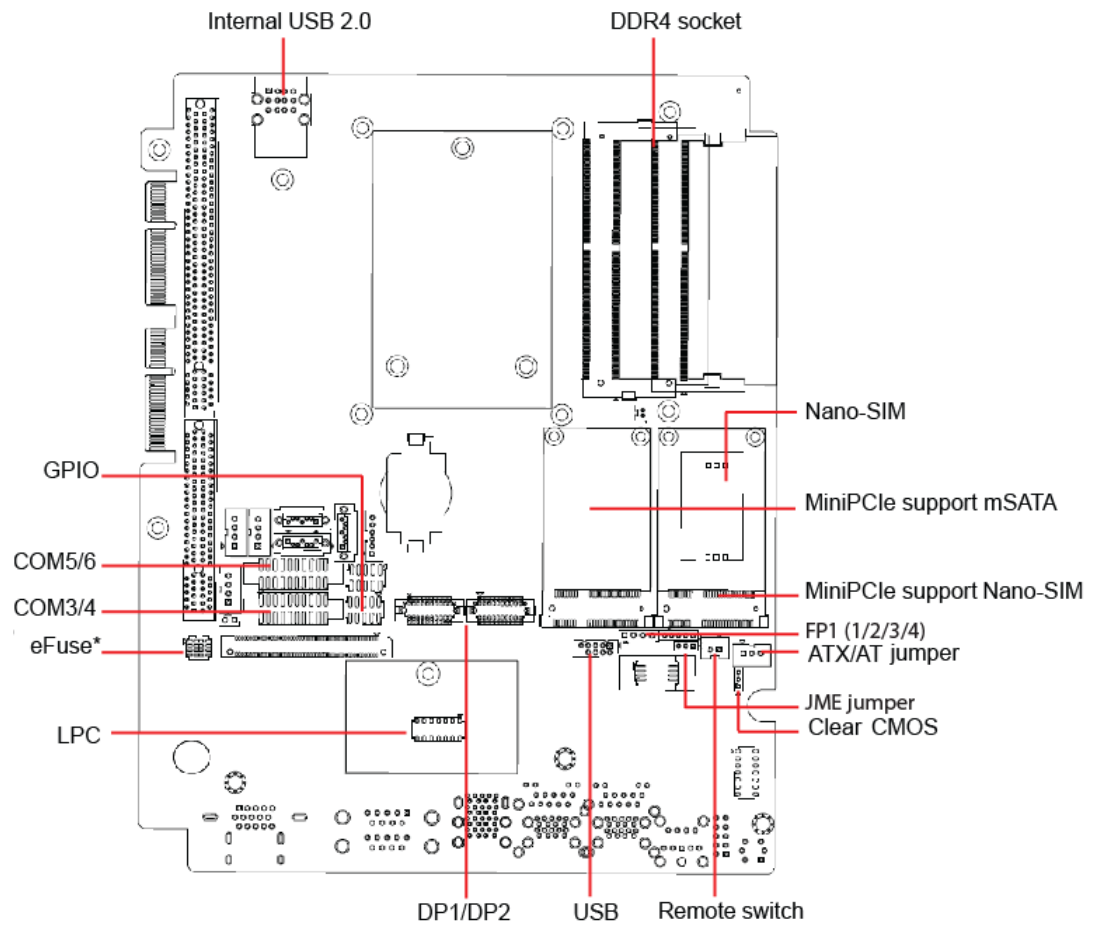
B: Bracket for expansion module

1. Undo the 4 screws and remove the bottom cover.
2. Undo the HDD tray & expansion module baffle.



3. Remove the baffle cover.
4. Assemble the module on the M/B. (Note: Optional expansion modules need to be connected with a cable.) (Please refer to M/B internal I/O connector specifications on the I/O connector page for the GPIO connector.)
5. Assemble the module baffle with screws.
6. Replace the bottom cover and secure with screws.

2.4.7 MIC-770 V2 Motherboard I/O Connector



Chapter 3

BIOS Operation

3.1 Introduction

With the AMI BIOS Setup Utility, you can modify BIOS settings and control specific features of your computer. The Setup Utility uses a number of menus for making changes and turning specific features on or off. This chapter describes the basic navigation of the MIC-770 V2 setup screens.

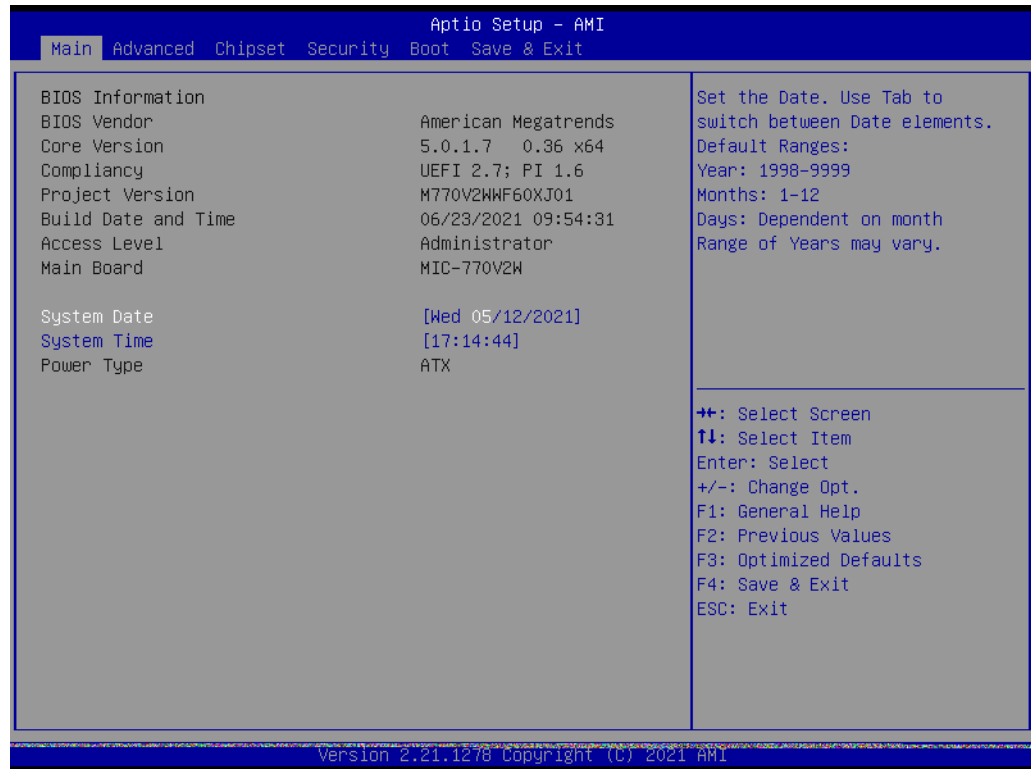


Figure 3.1 Main Setup Screen

The AMI BIOS ROM has a built-in setup program that allows users to modify basic system configuration. This information is stored in the NVRAM area so that it retains the setup information when the power is turned off.

3.2 Entering BIOS Setup

Press at bootup to enter the AMI BIOS Setup Utility, the main menu will appear on the screen. Use the arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.

The Main BIOS setup screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured; options in blue can be. The right frame displays the key legend.

The key legend above is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it.

3.2.1 Main Menu

When users first enter the BIOS Setup Utility, they enter the Main setup screen. Users can always return to the Main setup screen by selecting the Main tab. There are two Main setup options described in this section. The Main BIOS setup screen is shown below.

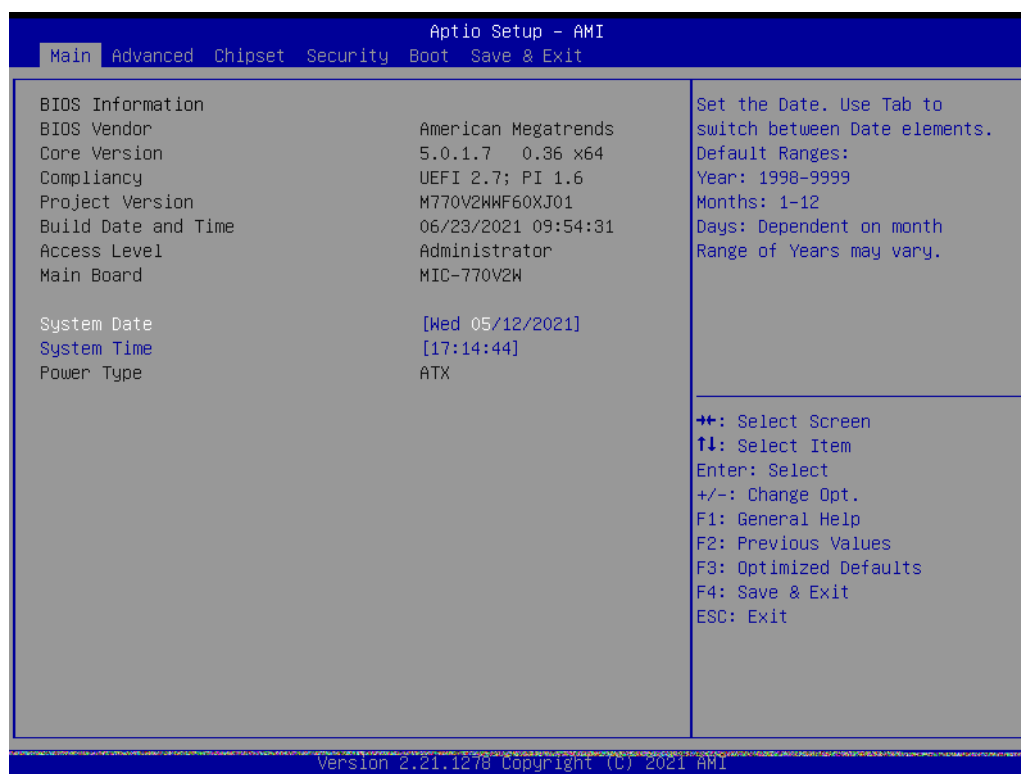


Figure 3.2 Main Setup Screen

The Main BIOS setup screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured; options in blue can be. The right frame displays the key legend.

The key legend above is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it.

■ System Time / System Date

Use this option to change the system time and date. Highlight System Time or System Date using the <Arrow> keys. Enter new values through the keyboard. Press the <Tab> key or the <Arrow> keys to move between fields. The date must be entered in MM/DD/YY format. The time must be entered in HH:MM:SS format.

3.2.2 Advanced BIOS Features Setup

Select the Advanced tab from the MIC-770 V2 setup screen to enter the Advanced BIOS setup screen. You can select any of the items in the left frame of the screen, such as CPU configuration, to go to the sub-menu for that item. You can display an Advanced BIOS Setup option by highlighting it using the <Arrow> keys. All Advanced BIOS setup options are described in this section. The Advanced BIOS setup screens are shown below. The sub-menus are described on the following pages.

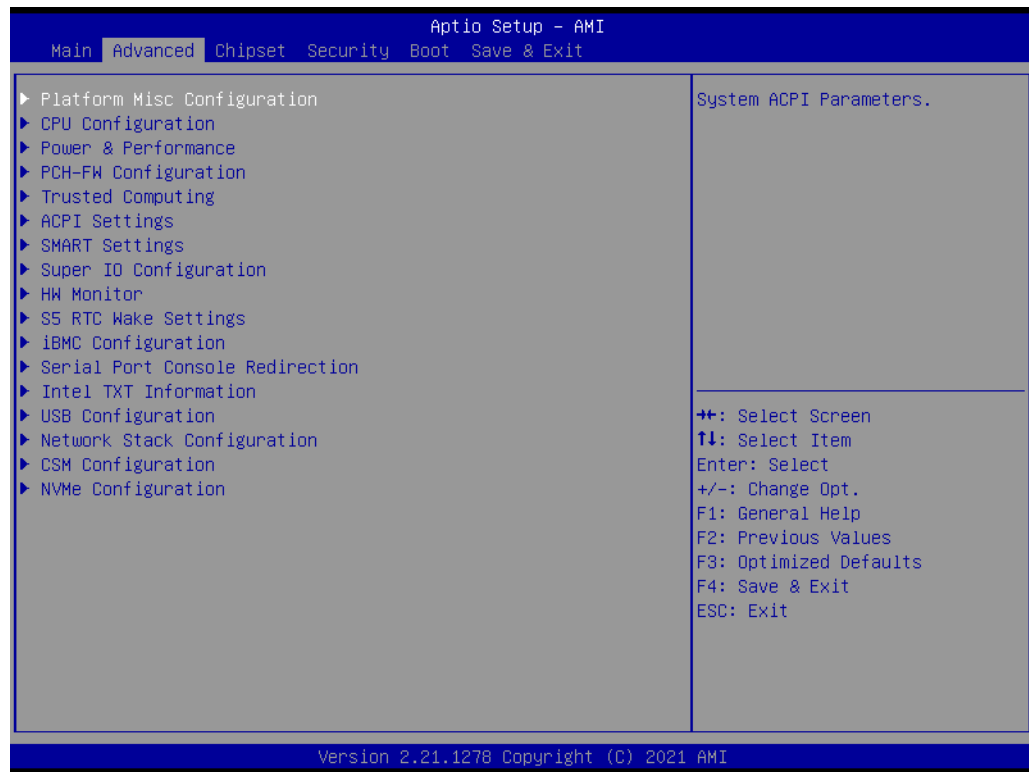


Figure 3.3 Advanced BIOS Features Setup Screen

3.2.2.1 iBMC Configuration



Figure 3.4 iBMC Configuration

■ iBMC

Enable or Disable the iBMC controller's hardware communication. The default setting is Enabled. The iBMC controller/function can be disabled if the item is selected as Disabled.

3.2.2.2 Platform Misc Configuration

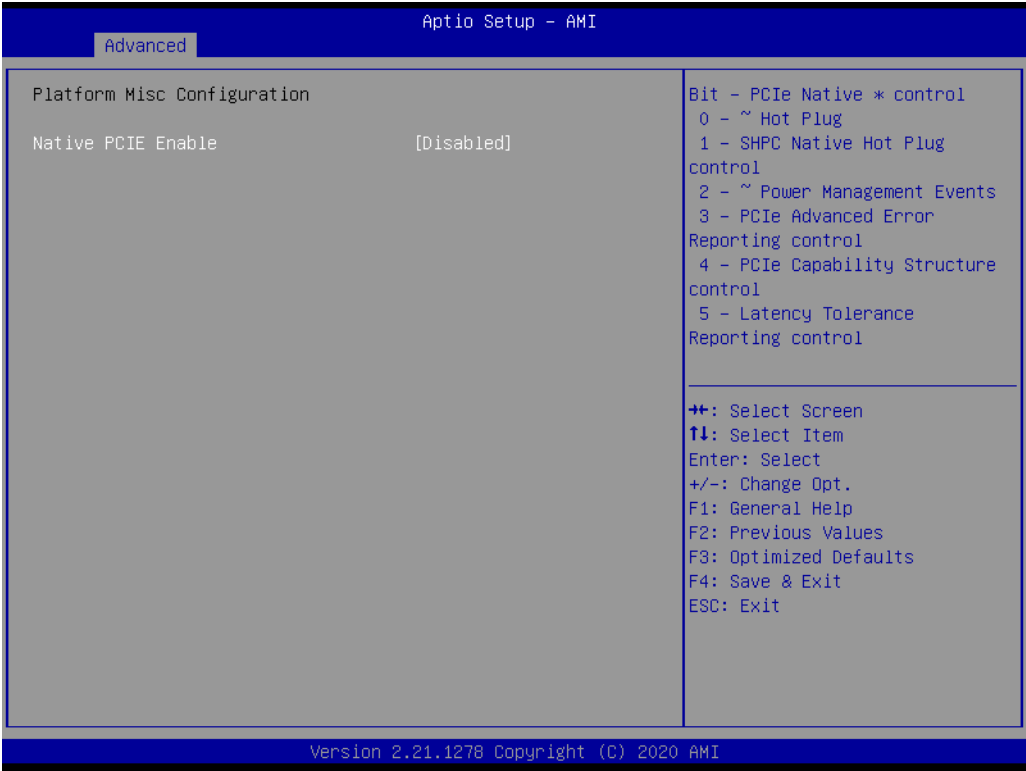


Figure 3.5 Platform Misc Configuration

- **Native PCIE Enable**
Enable or Disable PCI Express native support.

3.2.2.3 CPU Configuration

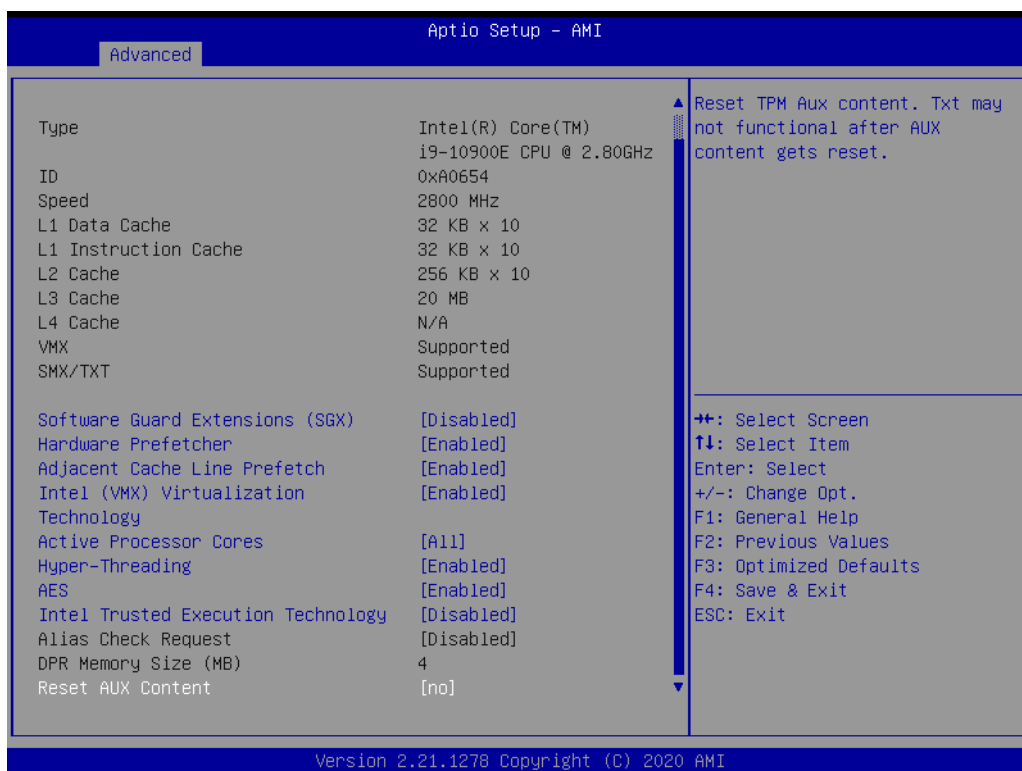
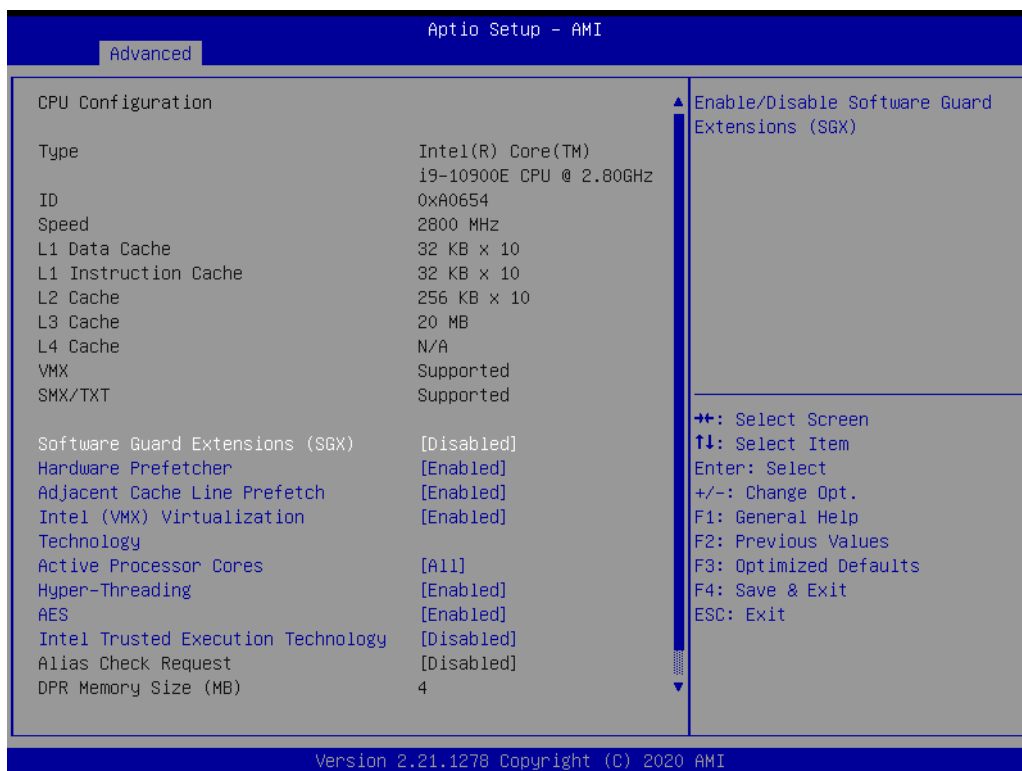


Figure 3.6 CPU Configuration

■ Software Guard Extensions (SGX)

Enable or Disable Software Control of Intel® Software Guard Extensions.

- **Hardware Prefetcher**

Hardware Prefetch is a technique that fetches instructions and/or data from memory into the CPU cache memory well before the CPU needs it to improve the load-to-use latency. You may choose to Enable or Disable it.

- **Adjacent Cache Line Prefetch**

The Adjacent Cache-Line Prefetch mechanism, like automatic hardware prefetch, operates without programmer intervention. When it is enabled through the BIOS, two 64-byte cache lines are fetched into a 128-byte sector, regardless of whether the additional cache line has been requested or not. You may choose to Enable or Disable it.

- **Intel® Virtualization Technology**

This feature is used to Enable or Disable the Intel® Virtualization Technology (IVT) extension. It allows multiple operating systems to run simultaneously on the same system by creating virtual machines, each running its own x86 operating system.

- **Active Processor Core**

Use this item to select the number of processor cores you want to activate when you are using a multi-core processor.

- **Hyper-Threading**

Enable or Disable Intel® Hyper-Threading technology.

- **AES**

Enable or Disable CPA advanced encryption standard instruction.

- **Intel® Trusted Execution Technology**

Enable or Disable utilization of additional hardware capabilities provided by Intel® Trusted Execution Technology. Changes require a full power cycle to take effect.

- **Reset AUX Content**

Reset TPM AUX content. TXT may not be functional after AUX content gets reset.

3.2.2.4 Power & Performance

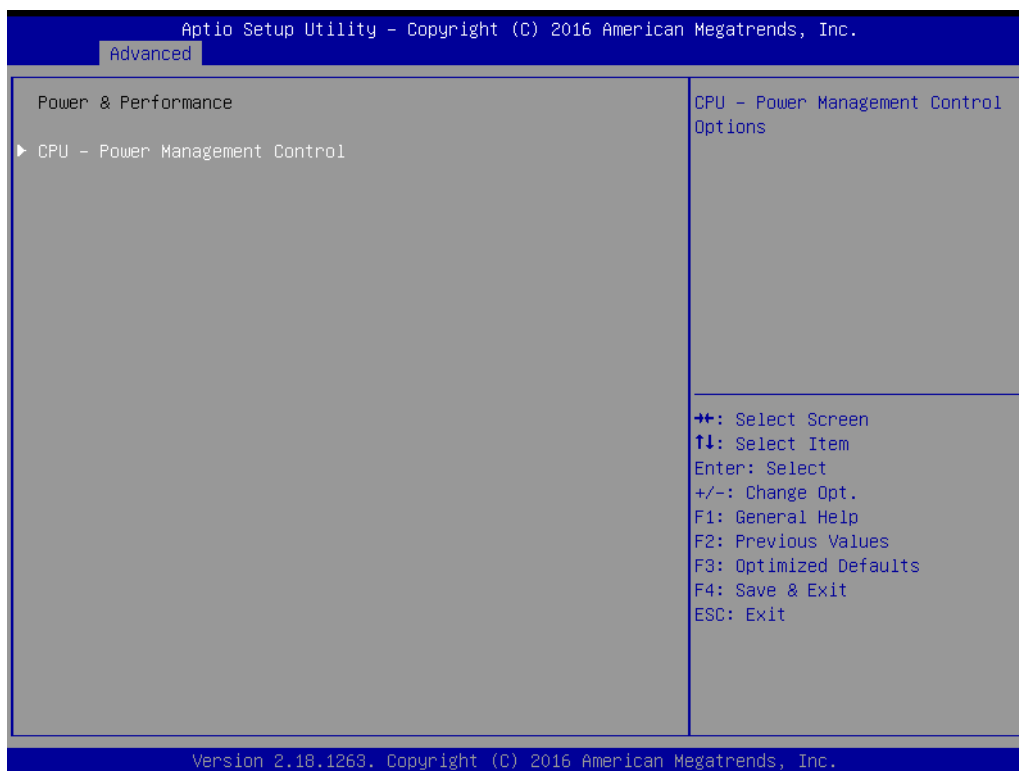


Figure 3.7 Power & Performance

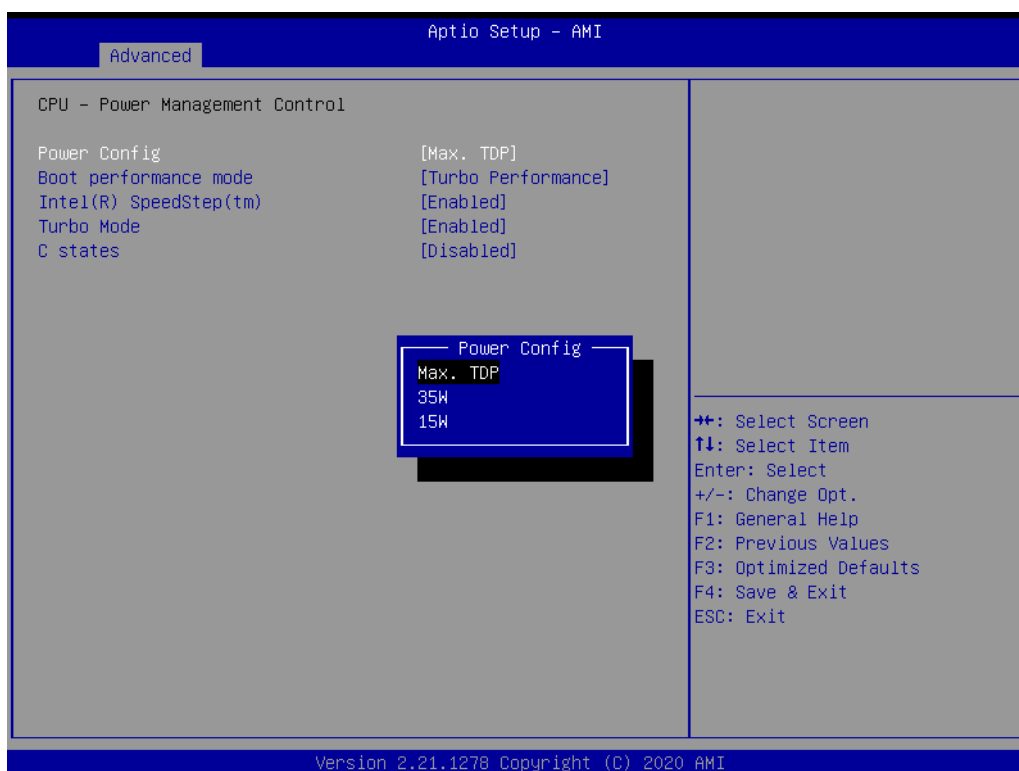


Figure 3.8 CPU - Power Management Control

- **Power Config**
 Default is Max. TDP. It enables user to adjust CPU TDP to 35W or 15W according to the user's requirements.

- **Boot Performance**
Select the performance state that the BIOS will set to before OS handoff.
- **Intel® Speedstep™**
Allows more than two frequency ranges to be supported.
- **Turbo Mode**
Enable or Disable processor turbo mode.
- **C states**
Intel® C states setting for power saving.

3.2.2.5 PCH-FW Configuration

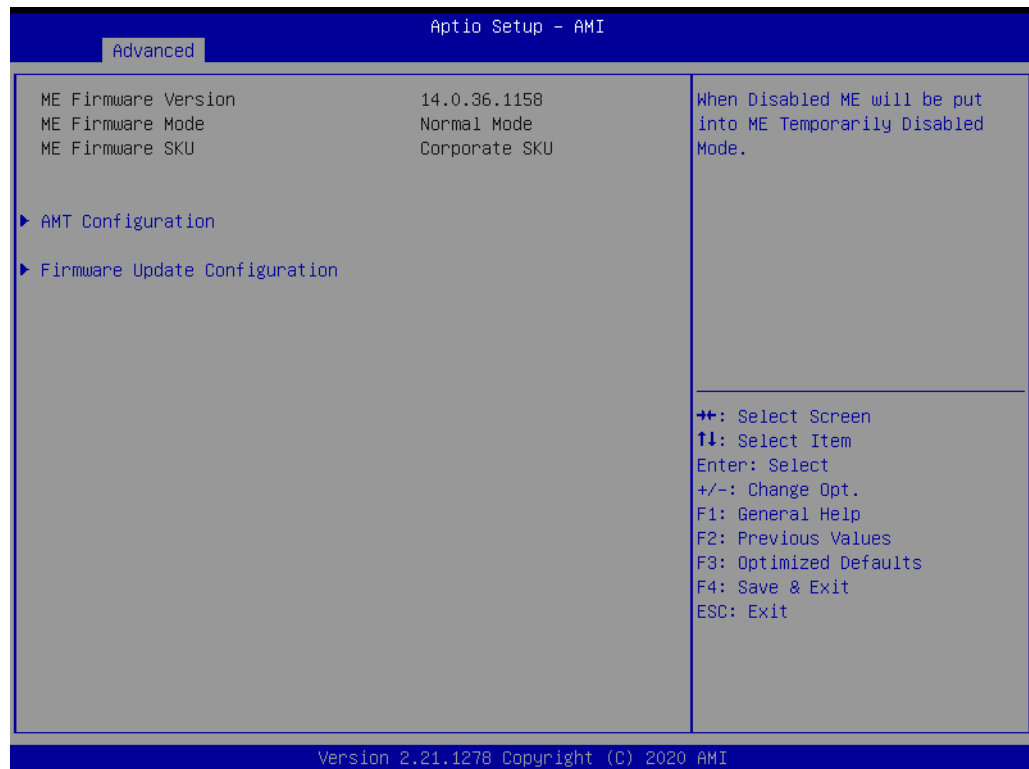


Figure 3.9 PCH-FW Configuration

- **PCH-FW Version**
The PCH-FW page shows Intel® ME FW information.

■ AMT Configuration

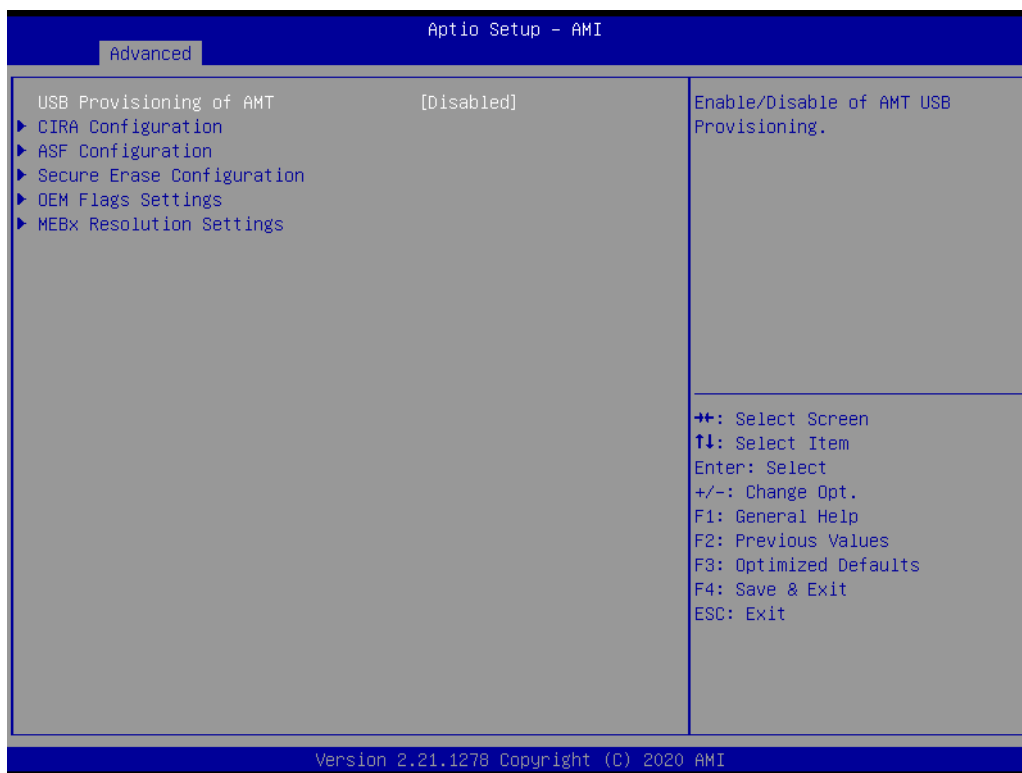


Figure 3.10 AMT Configuration

■ CIRA Configuration



Figure 3.11 CIRA Configuration

- **Activate the Remote Assistance Process**
Trigger CIRA boot.

■ ASF Configuration

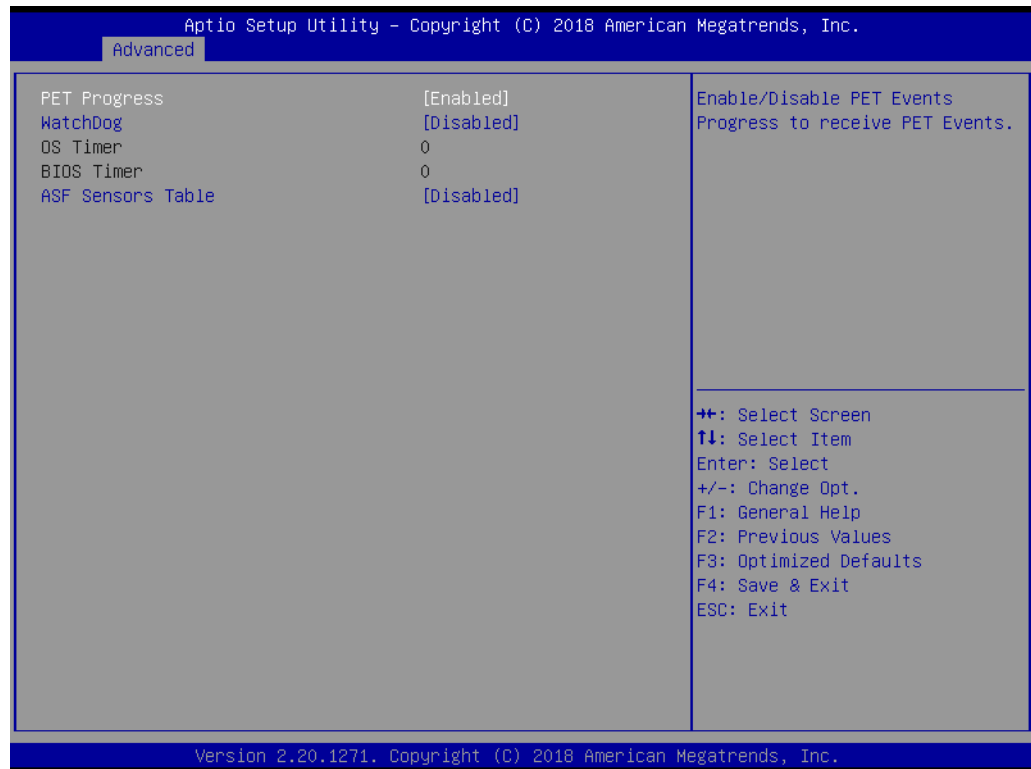


Figure 3.12 ASF Configuration

- **PET Progress**
Enable or Disable PET events Progress to receive PET events.
- **Watch Dog**
Enable or Disable the Watchdog Timer.
- **ASF Sensors Table**
Enable or Disable to add the ASF Sensor Table into the ASF ACPI Table.

■ Secure Erase Configuration

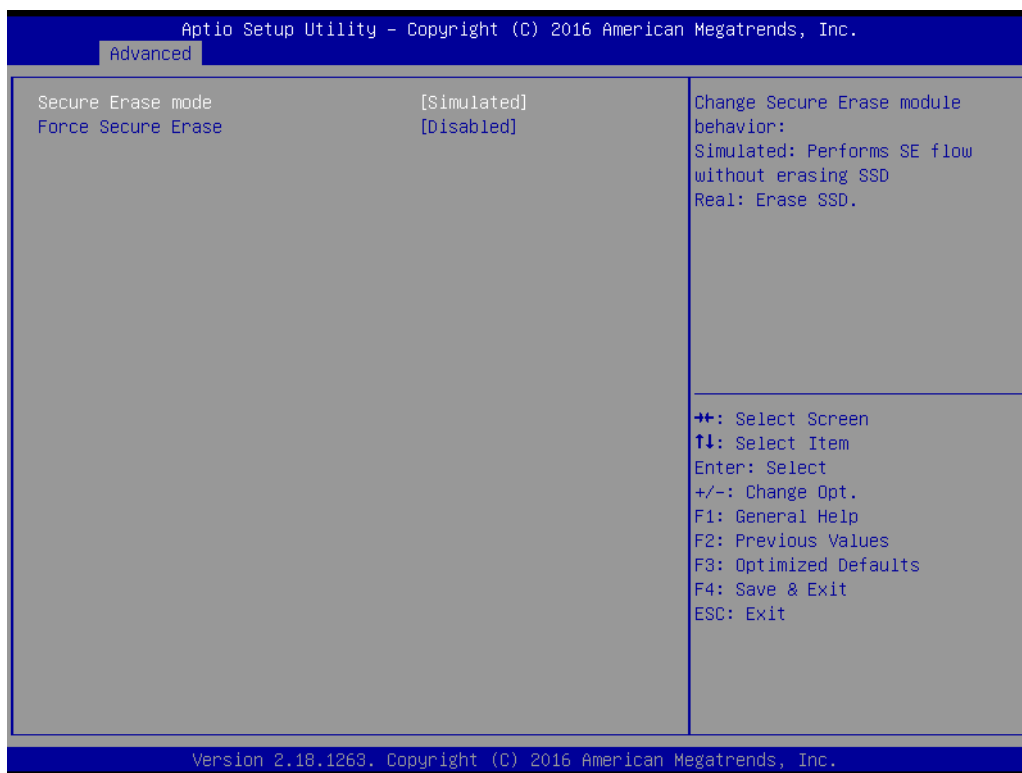


Figure 3.13 Secure Erase Configuration

- **Secure Erase mode**

Change Secure Erase module behavior as Simulated or Real.

- **Force Secure Erase**

Enable or Disable Force Secure Erase on next boot.

■ OEM Flag Settings

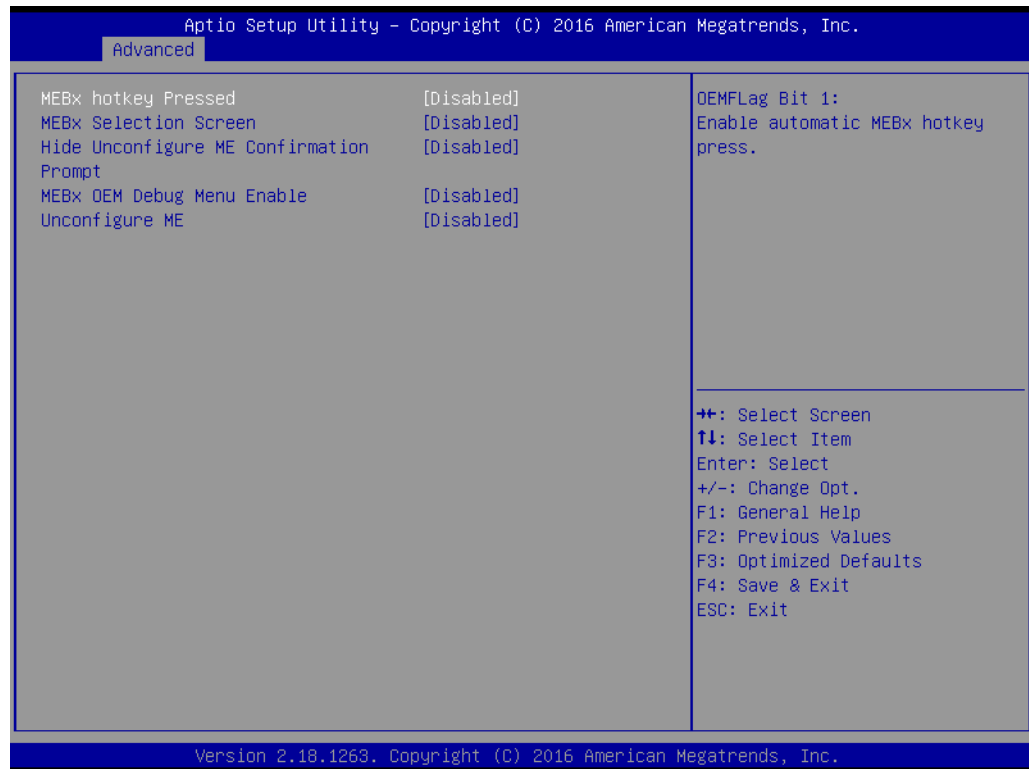


Figure 3.14 OEM Flag Settings

- **MEBx hotkey Pressed**
Enable or Disable automatic MEBx hotkey press.
- **MEBx Selection Screen**
Enable or Disable the MEBx Selection Screen.
- **Hide Un-Configure ME Confirmation Prompt**
Hide Un-Configure ME without the password confirmation prompt.
- **MEBx OEM Debug Menu Enable**
Enable or Disable the OEM debug menu in MEBx.
- **Unconfigure ME**
Un-configure ME without password.

■ MEBx Resolution Settings

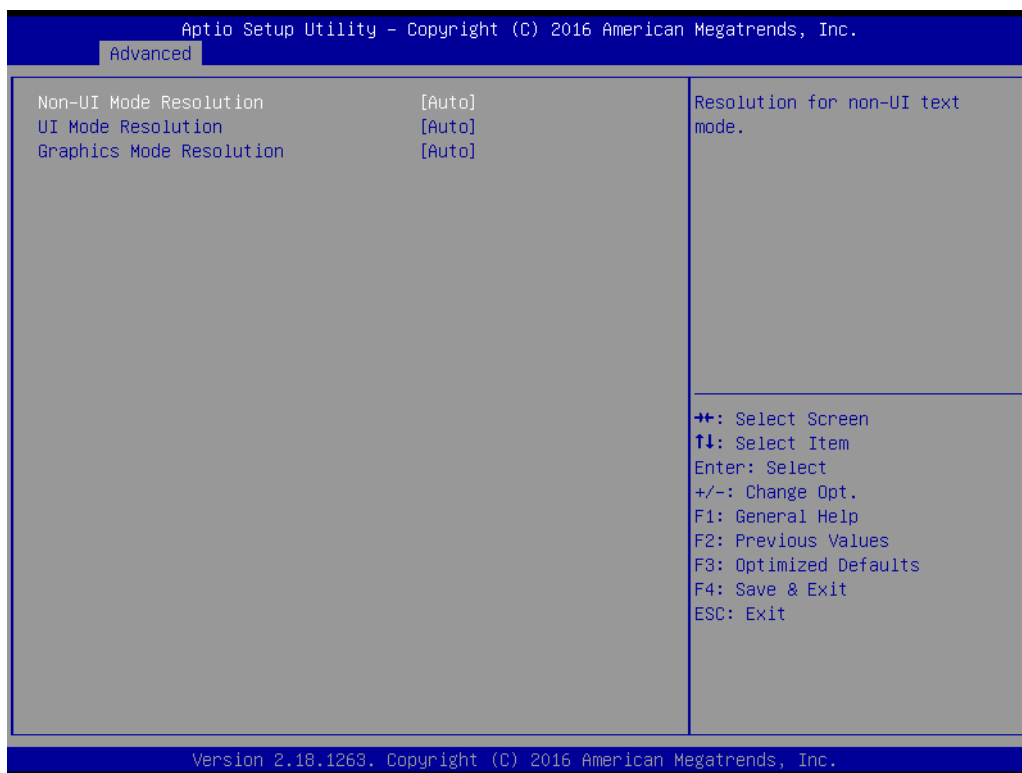


Figure 3.15 MEBx Resolution Settings

- **Non-UI Mode Resolution**
Set resolution for non-UI text mode.
- **UI Mode Resolution**
Set resolution for UI text mode.
- **Graphics Mode Resolution**
Set resolution for graphics mode.

■ Firmware Update Configuration



Figure 3.16 Firmware Update Configuration

– ME FW Image Re-flash

Enable or Disable the ME firmware image re-flash function.

3.2.2.6 Trusted Computing



Figure 3.17 TPM Settings

■ TPM Support

Enable or Disable TPM Support. You can purchase the Advantech LPC TPM module to enable the TPM function. P/N: PCA-TPM-00B1E.

3.2.2.7 ACPI Settings

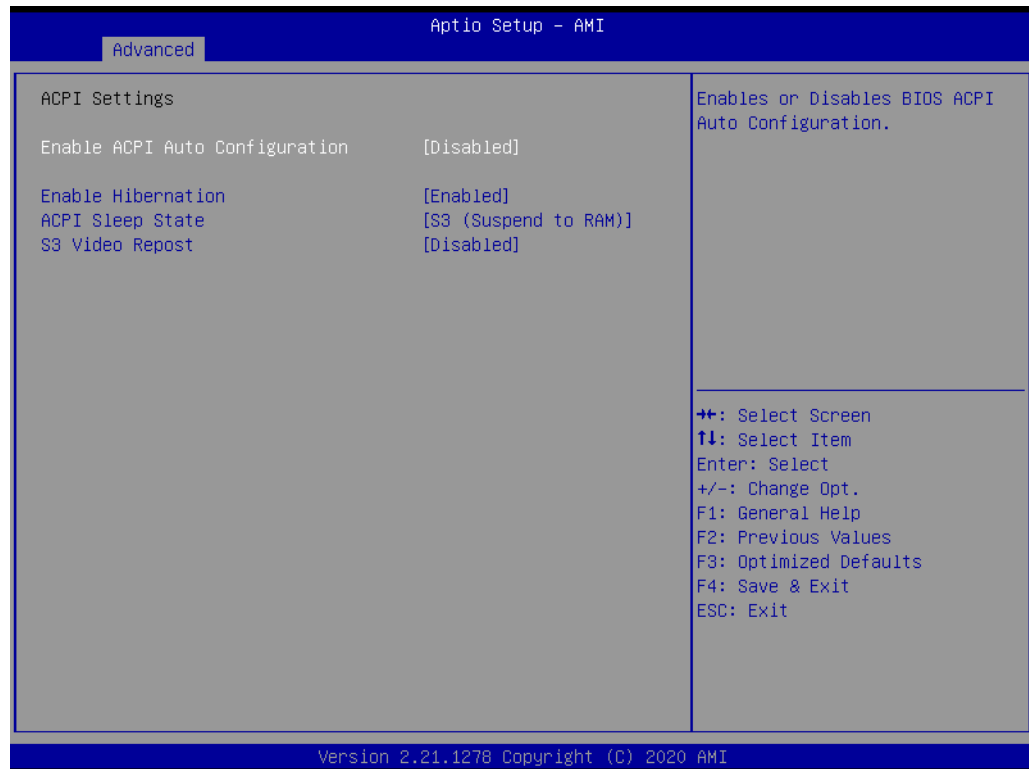


Figure 3.18 ACPI Settings

- **Enable ACPI Auto Configuration**
Enable or Disable ACPI auto configuration.
- **Enable Hibernation**
Enable or Disable Hibernation (OS/S4 Sleep State). This option may not be applied in some OS.
- **ACPI Sleep State**
Auto or S1 only or S3 only ACPI Sleep State.
- **S3 Video Repost**
Enable or Disable S3 Video Repost.

3.2.2.8 SMART Settings

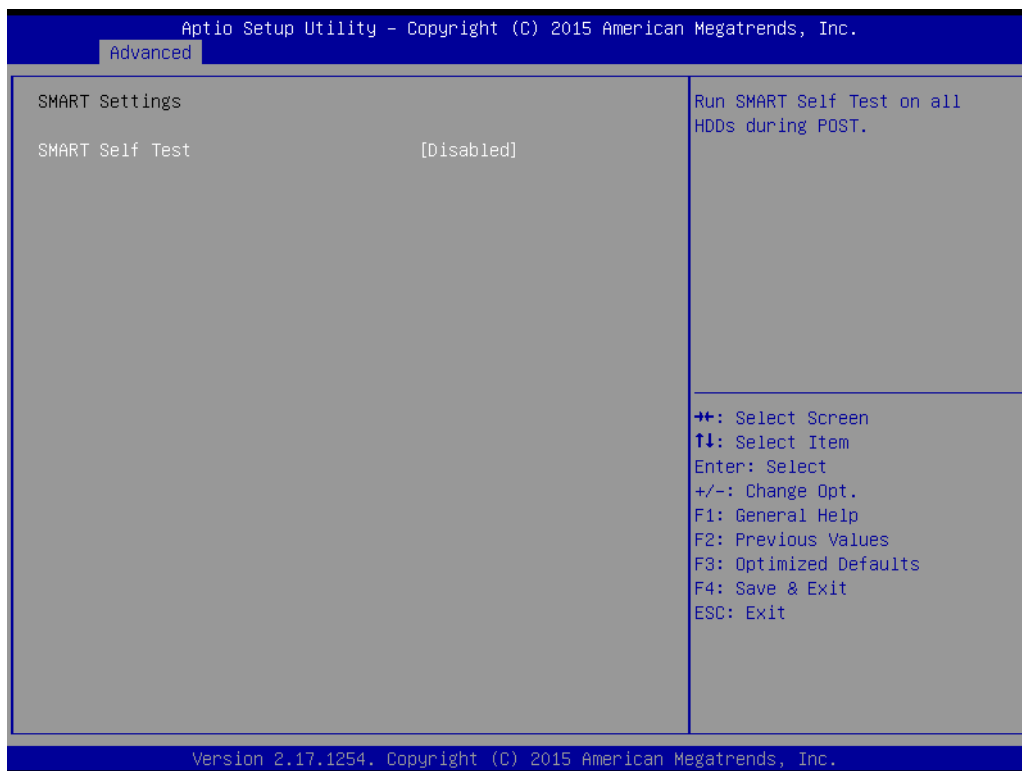


Figure 3.19 SMART Settings

■ **SMART Self Test**

Enable or Disable SMART Self Test on all HDDs during POST.

3.2.2.9 Super IO Configuration

MIC-770 V2 supports 2 x RS-232/422/485 on the front side. MIC-770 has 4 more RS-232 (Serial Port 3, 4, 5, 6) via 2 x DB9 cables in the accessory box.



Figure 3.20 Super IO Configuration

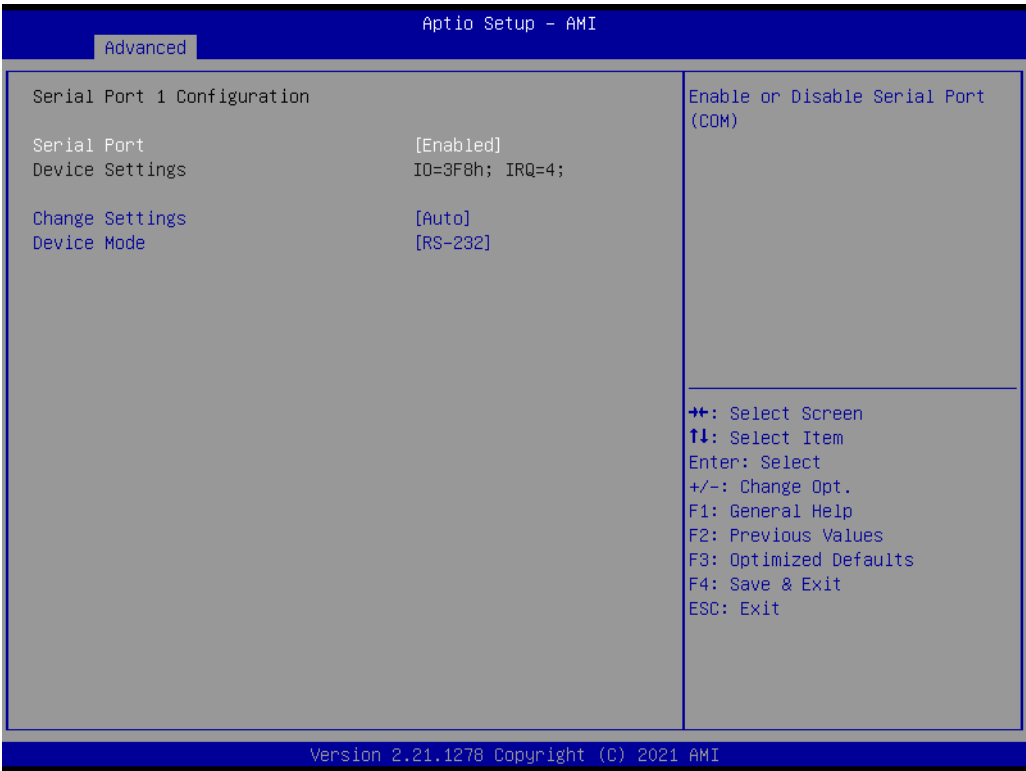
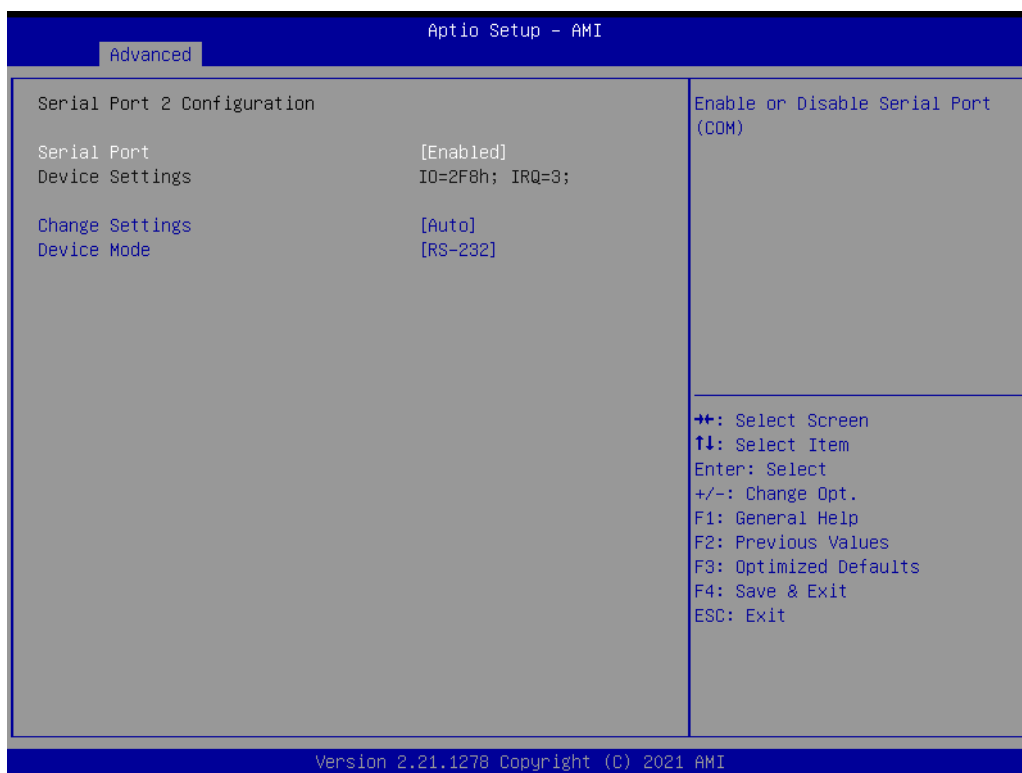
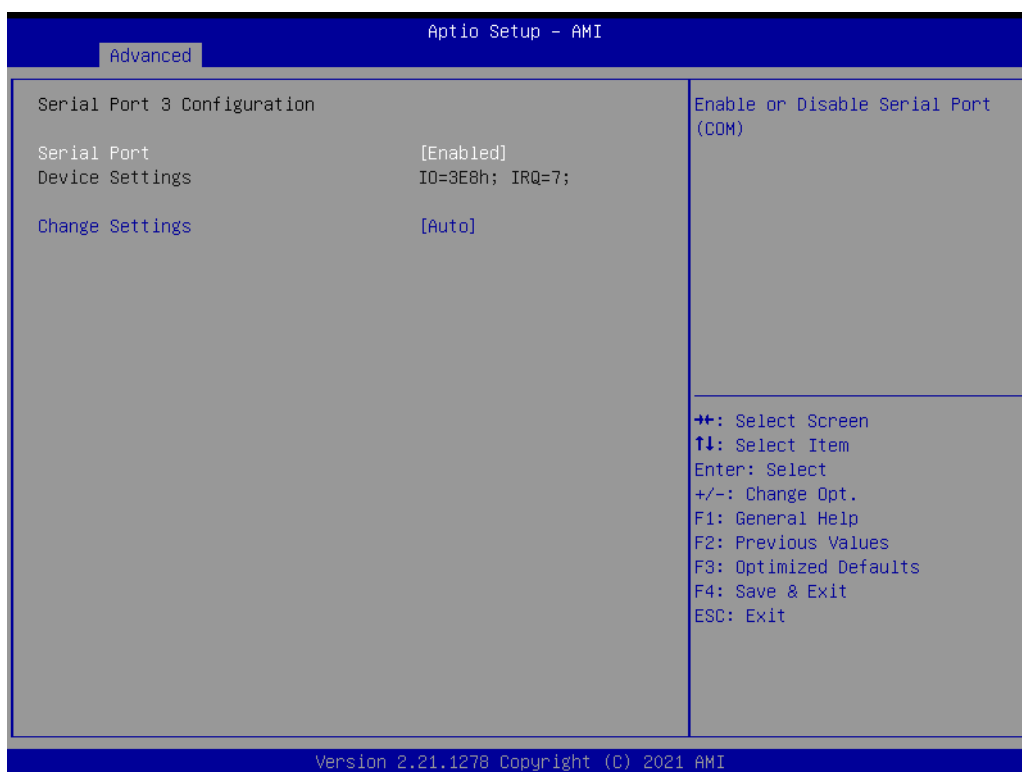


Figure 3.21 Serial Port 1 Configuration

**Figure 3.22 Serial Port 2 Configuration****Figure 3.23 Serial Port 3 Configuration**

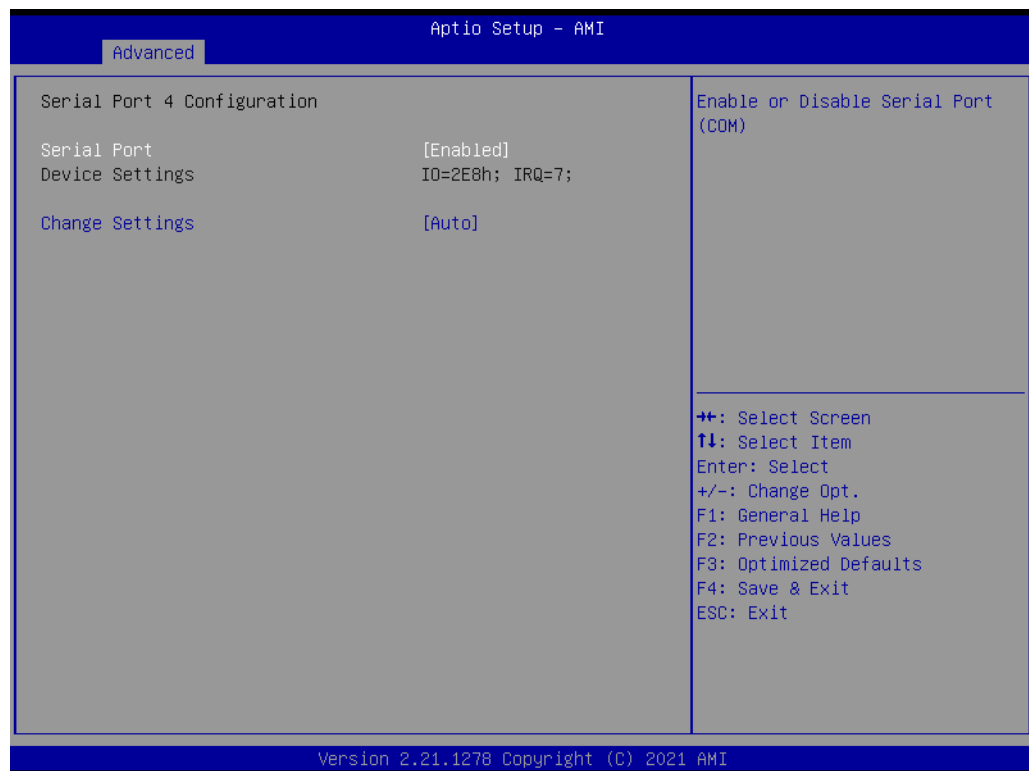


Figure 3.24 Serial Port 4 Configuration

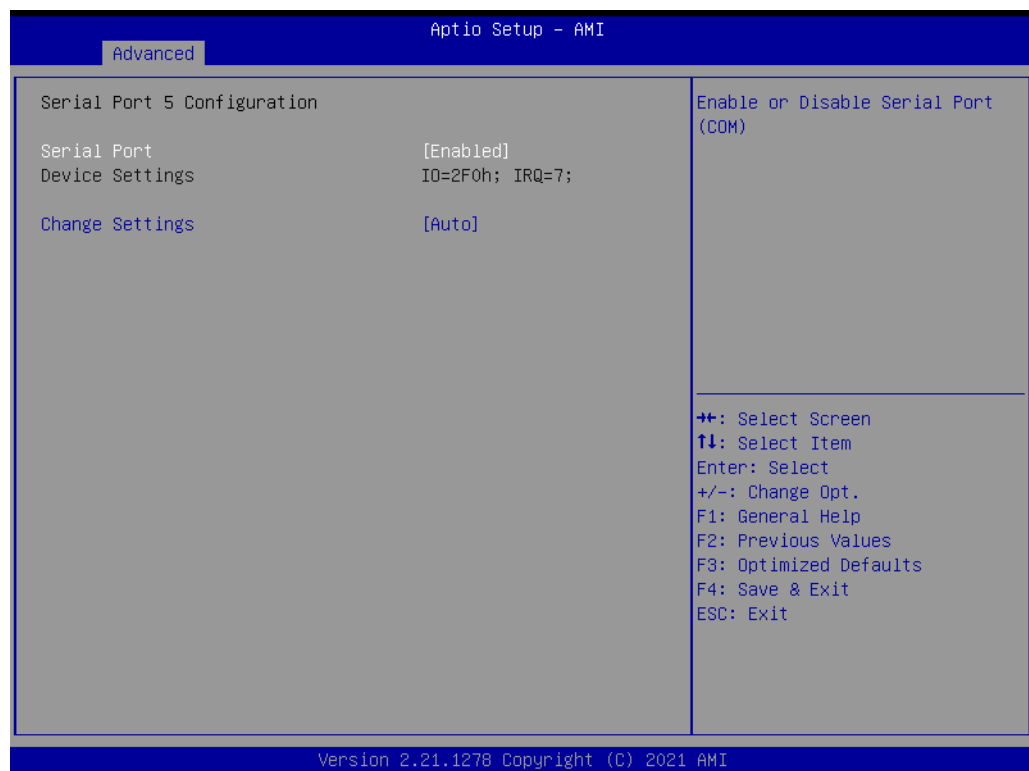


Figure 3.25 Serial Port 5 Configuration

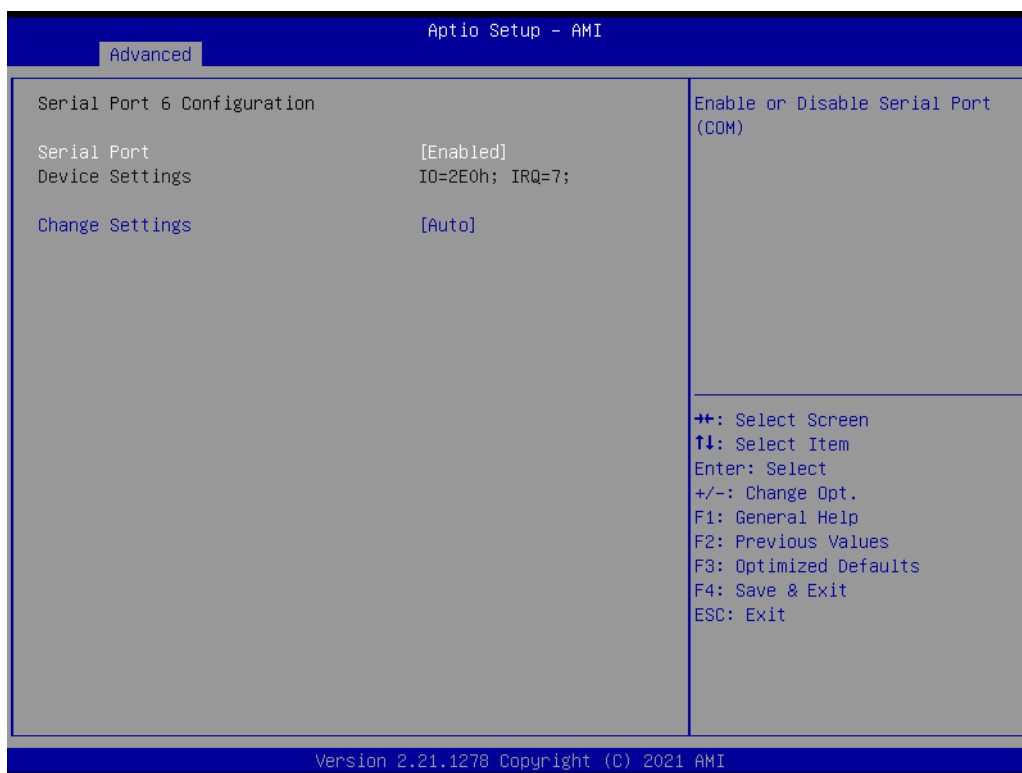


Figure 3.26 Serial Port 6 Configuration

- **Serial Port 1 Configuration**
 - **Serial Port**
Enable or Disable Serial Port 1.
 - **Change Settings**
Select an optimal setting for Serial Port 1.
- **Serial Port 2 Configuration**
 - **Serial Port**
Enable or Disable Serial Port 2.
 - **Change Settings**
Select an optimal setting for Serial Port 2.
- **Serial Port 3-6 Configuration**
 - **Serial Port**
This item allows users to Disable or Enable the serial port.
 - **Change Settings**
This item allows users to change settings of the serial ports. The default setting is Auto.

3.2.2.10 H/W Monitor

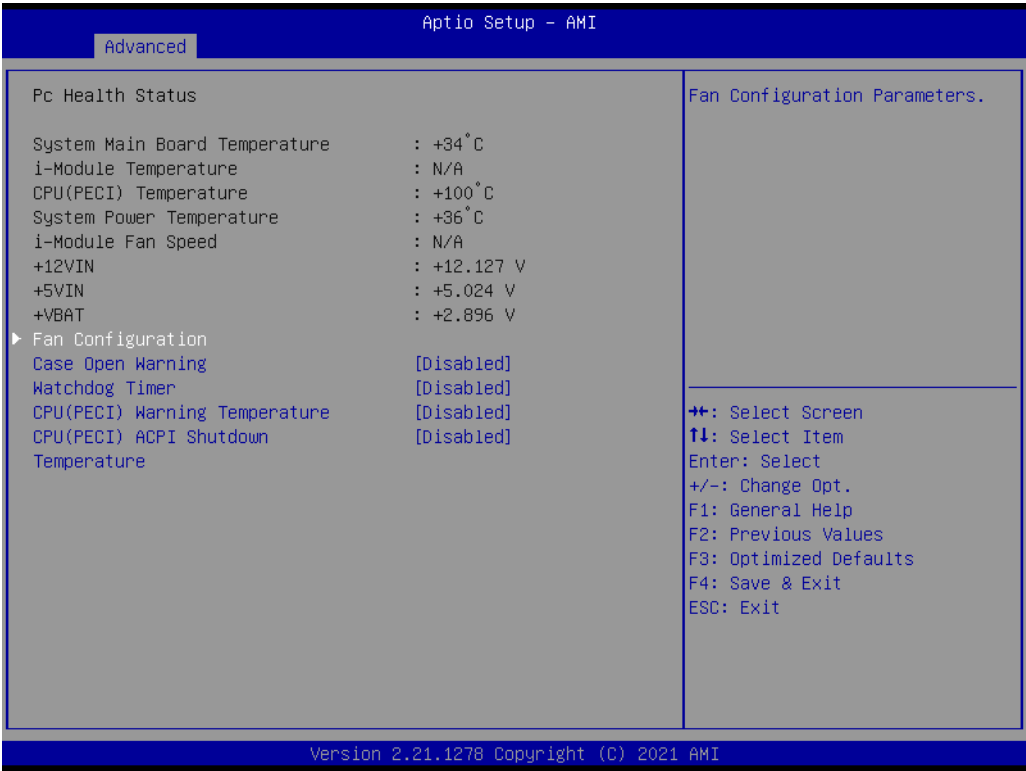
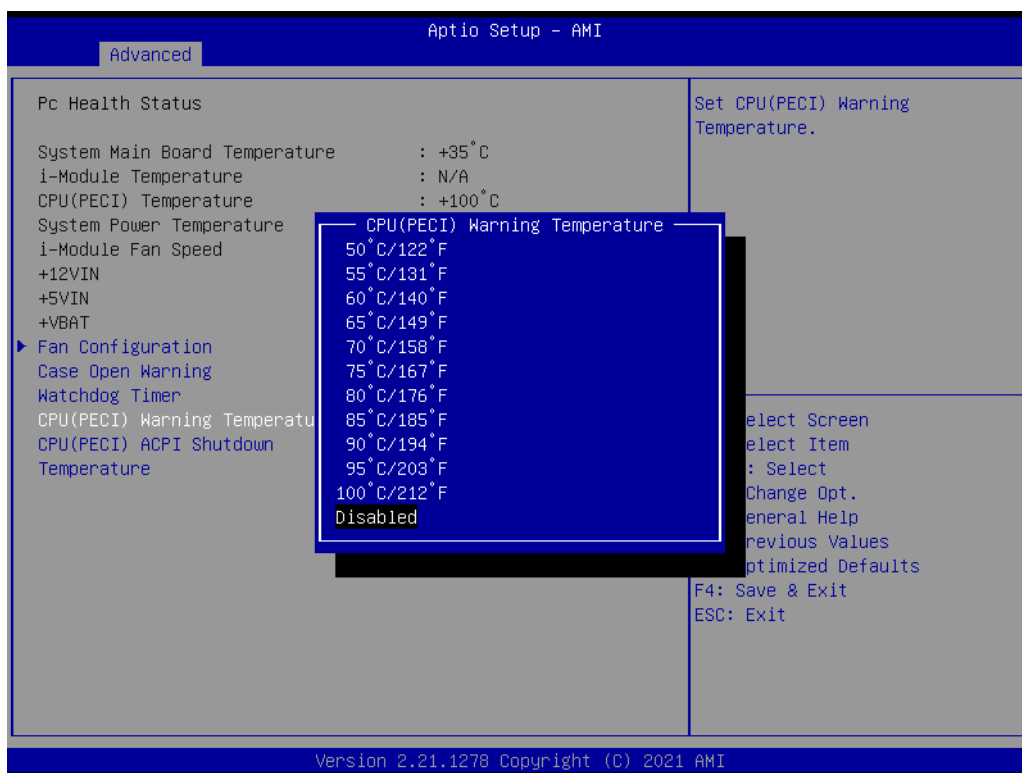


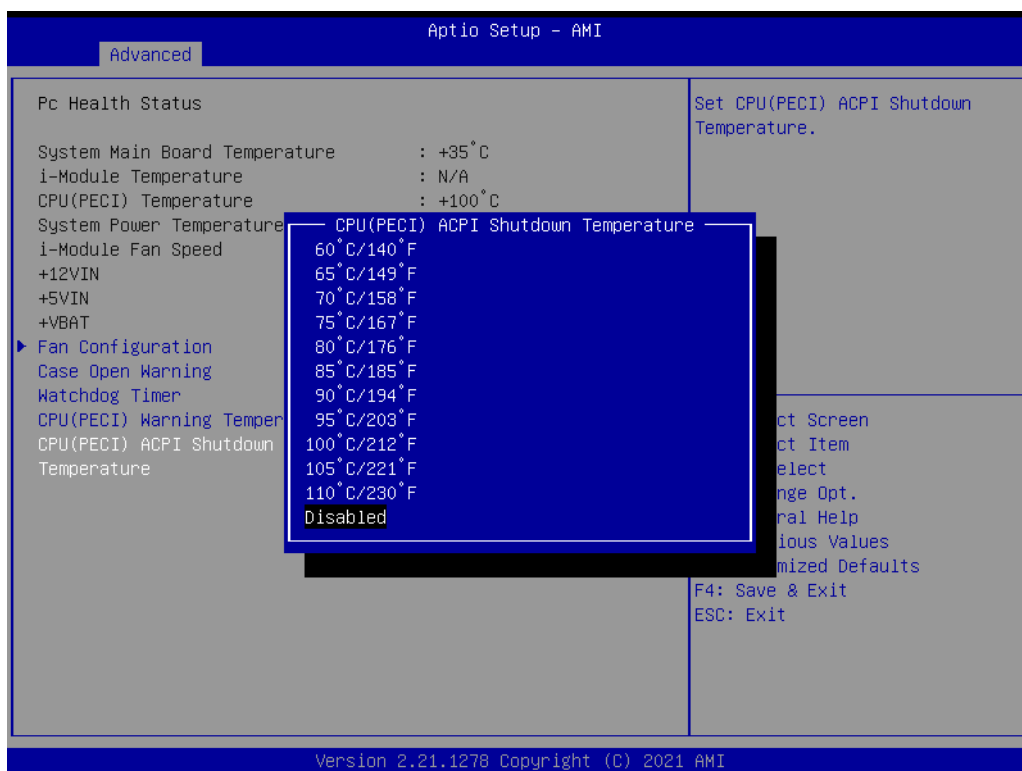
Figure 3.27 PC Health Status

- **Case Open Warning**
Enable or Disable the Chassis Intrusion monitoring function. When it is enabled and the case is opened, the speaker beeps.
- **CPU (PECI) Warning Temperature**
Use this item to set the CPU warning temperature. When the system reaches the warning temperature, the speaker will beep.



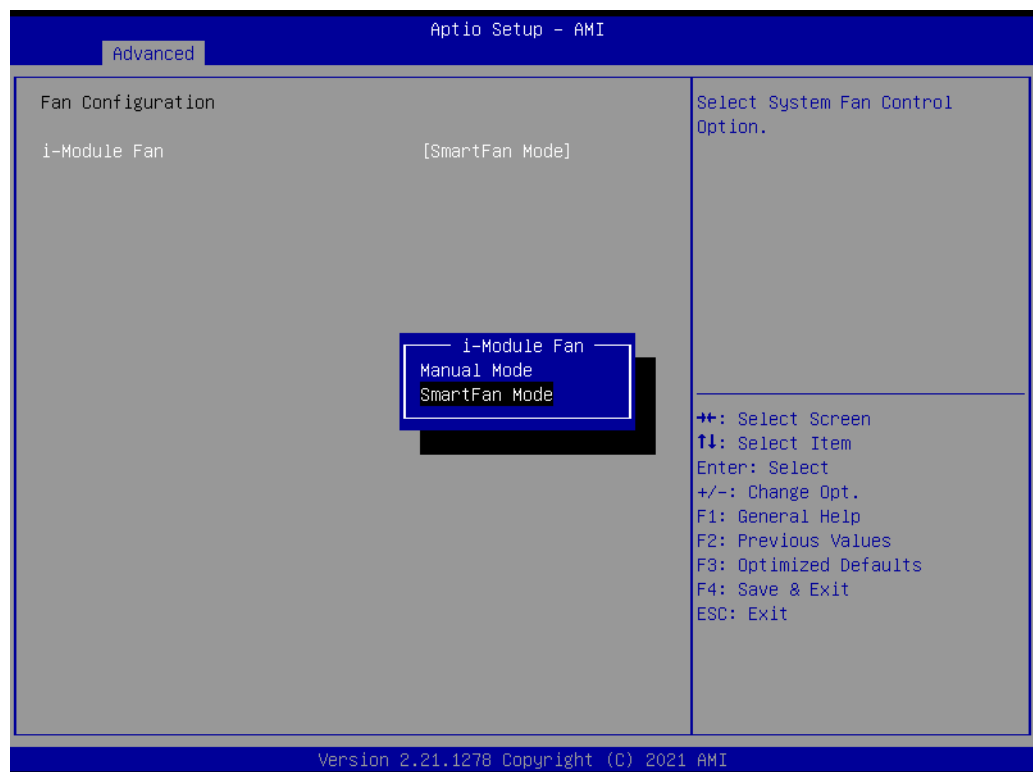
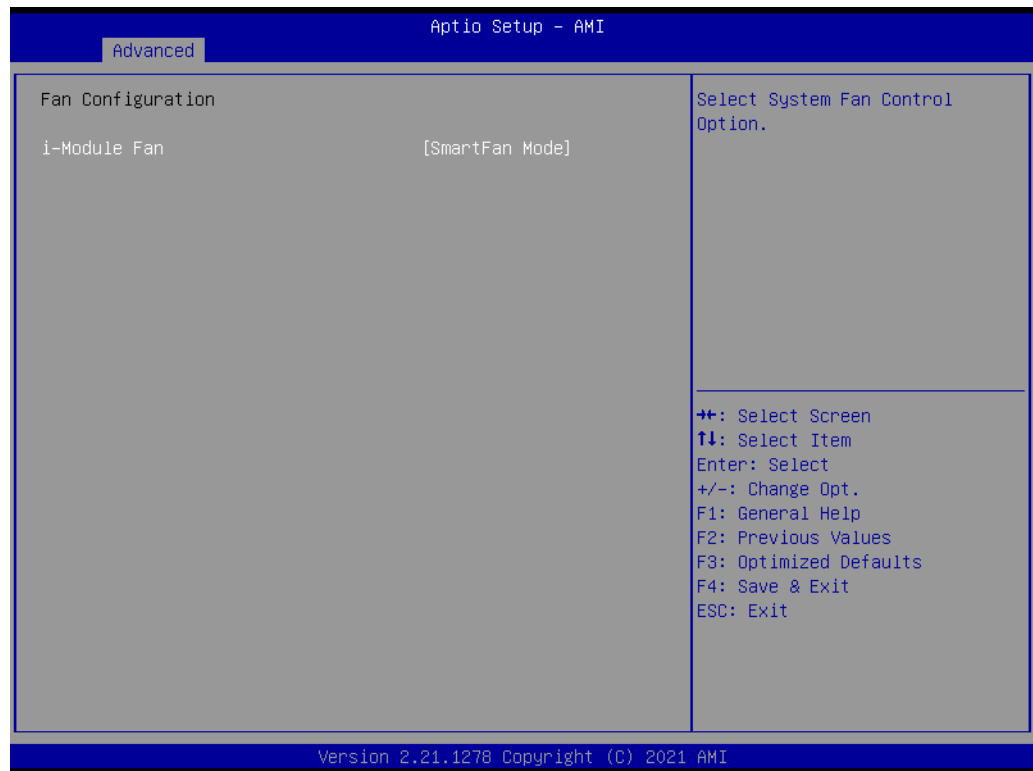
■ ACPI (PECI) Shutdown Temperature

Use this item to set the ACPI shutdown temperature. When the system reaches the shutdown temperature, it will be automatically shut down by ACPI OS to protect the system from overheat damage.



■ Fan Configuration

Select Manual Mode or SmartFan Mode for the i-Module FAN.





3.2.2.11 S5 RTC Wake Settings

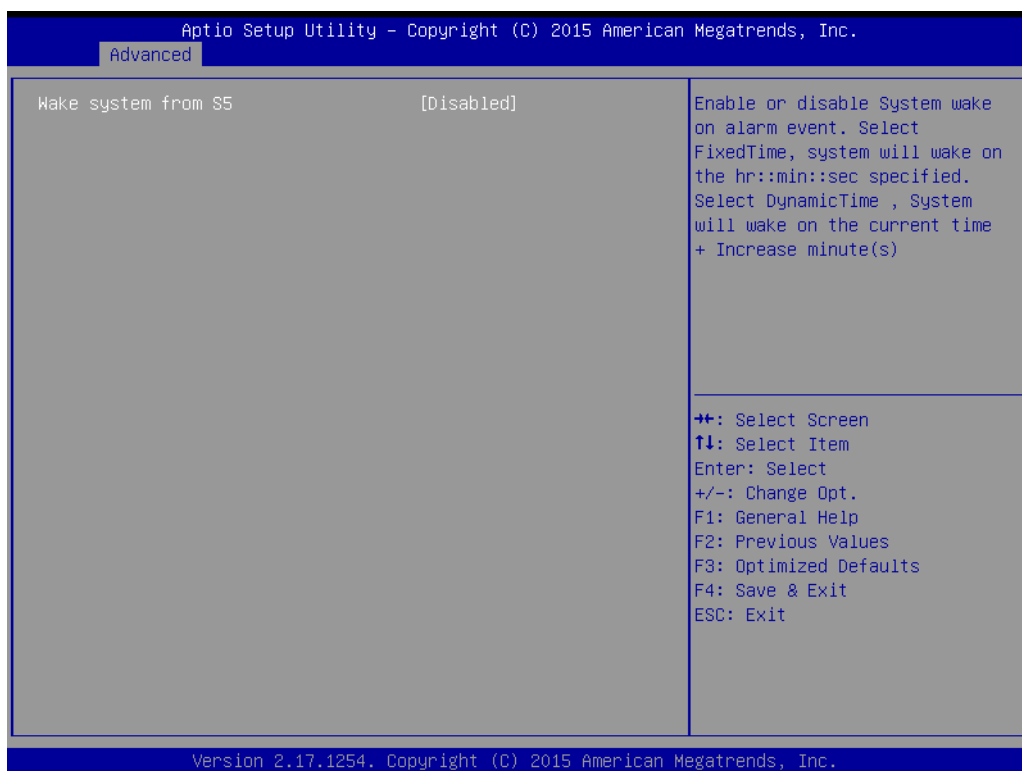


Figure 3.28 S5 RTC Wake Settings

- **Wake system with Fixed Time**

Enable or Disable System wake on alarm event. The system will wake on the hr:min:sec as specified.

3.2.2.12 Serial Port Console Redirection

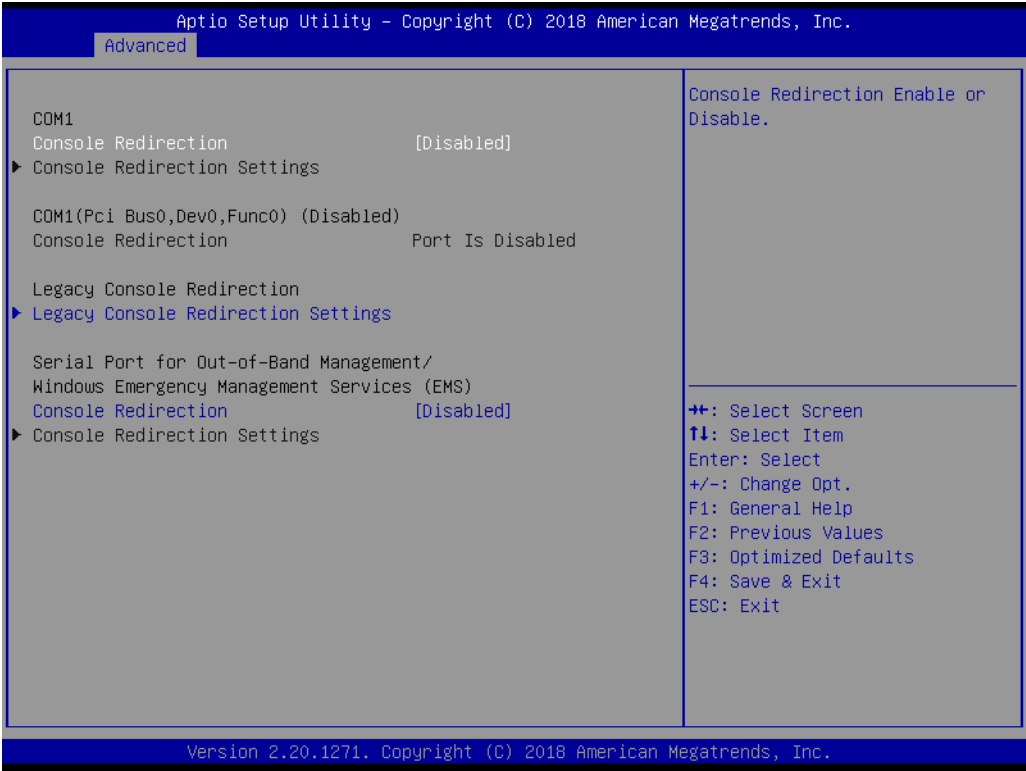


Figure 3.29 Serial Port Console Redirection

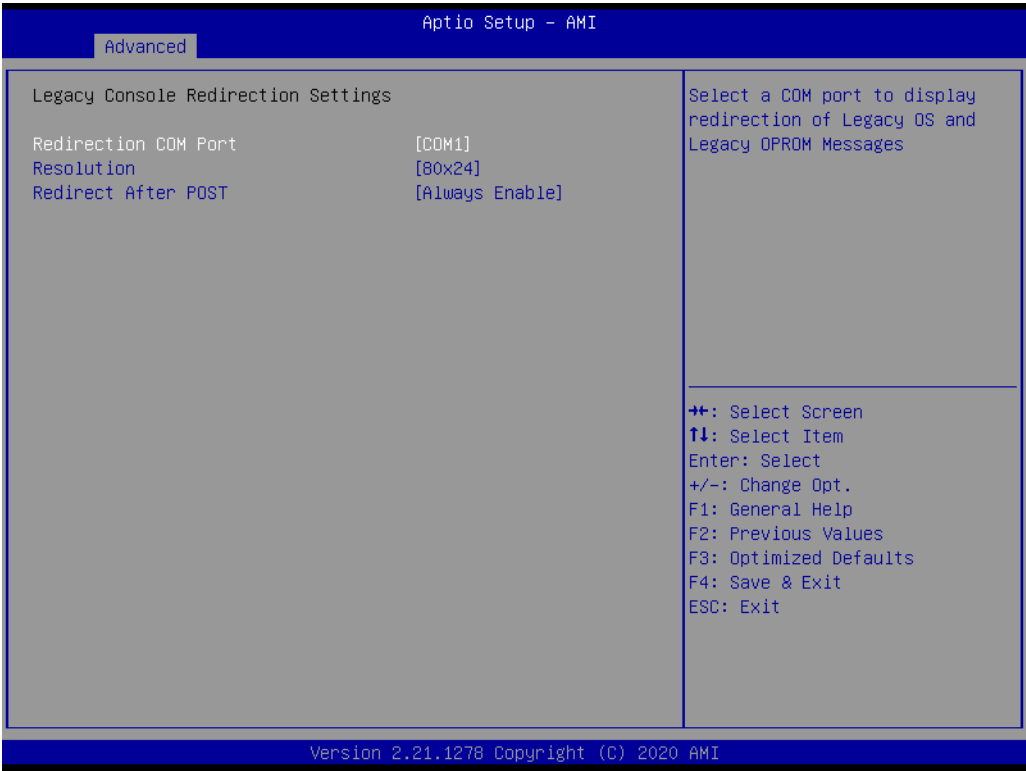


Figure 3.30 Legacy Console Redirection Settings

- **COM1**
 - **Console Redirection Settings**
 - Console Redirection Enable or Disable.

- **Legacy Console Redirection**
 - **Legacy Console Redirection Settings**
Select a COM port to display redirection of Legacy OS and Legacy OPRM messages.
- **Serial Port for Out-of-Band Management / Windows Emergency Management services (EMS)**
 - **Console Redirection Settings**
Enable or Disable Console Redirection.

3.2.2.13 Intel® TXT Information

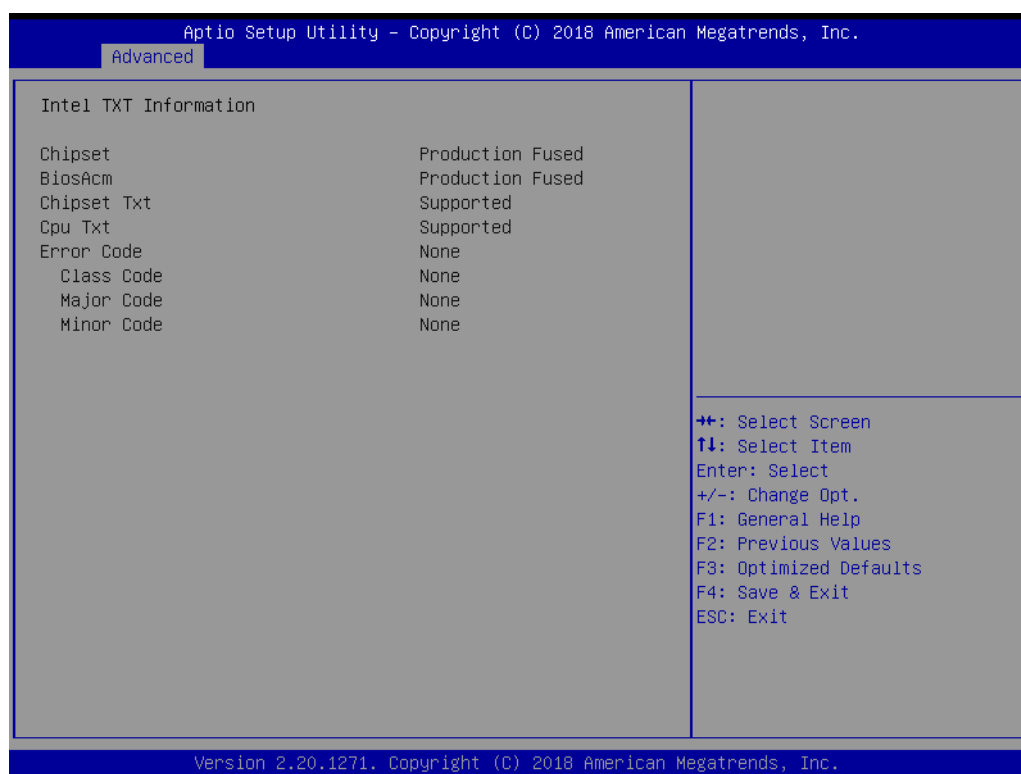


Figure 3.31 Intel® TXT Information

3.2.2.14 USB Configuration



Figure 3.32 USB Configuration

■ Legacy USB Support

This is to support USB devices under legacy OS such as DOS. When choosing "Auto", the system will automatically detect if any USB device is plugged into the computer and enable USB legacy mode when a USB device is plugged in and disable USB legacy mode when no USB device is plugged in.

■ XHCI Hand-off

This is a workaround for OS without XHCI hand-off support. The XHCI ownership change should be claimed by the XHCI driver.

■ USB Mass Storage Driver Support

Enable or Disable USB Mass Storage driver support.

■ USB transfer time-out

Allows you to select the USB transfer time-out value. [1, 5, 10, 20 sec]

■ Device reset time-out

Allows you to select the USB device reset time-out value. [10, 20, 30, 40 sec]

■ Device power-up delay

Maximum time the device will take before it properly reports itself to the Host Controller. "Auto" uses the default value: for a Root port it is 100 ms, for a Hub port the delay is taken from the hub descriptor.

■ Mass Storage Devices

Mass storage device emulation type. "Auto" enumerates devices according to their media format. Optical drives are emulated as "CD-ROM" drives and no media will be emulated according to a drive type.

3.2.2.15 Network Stack Configuration



Figure 3.33 Network Stack Configuration

- **Network Stack**
Enable or Disable UEFI Network Stack.

3.2.2.16 CSM Configuration



Figure 3.34 CSM Configuration

■ Compatibility Support Module Configuration

– CSM Support

Enable or Disable CSM Support. The default setting is "Disabled". If your graphics card does not support UEFI mode, make sure to select "Enabled" to allow non-UEFI boot mode before installing the graphics card to turn on the computer.

3.2.2.17 NVMe Configuration



Figure 3.35 NVMe Configuration

- **NVMe Configuration**
NVMe M.2 storage devices are supported.

3.2.3 Chipset

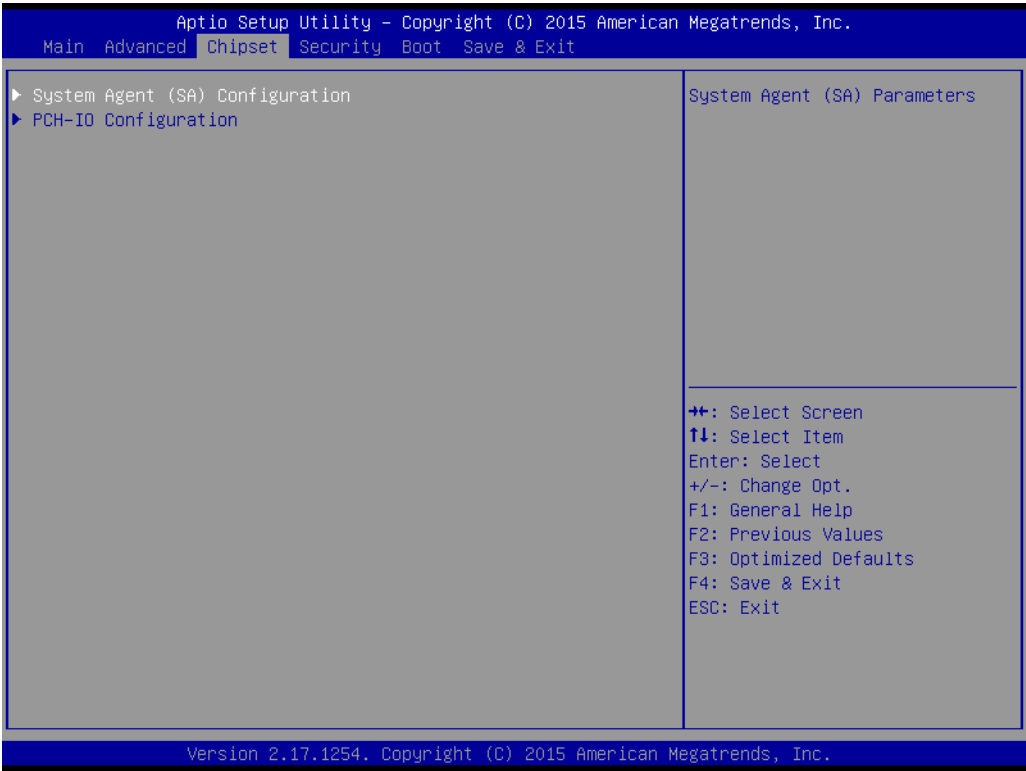


Figure 3.36 Chipset

This page provides information for the chipset on MIC-770 V2.

3.2.3.1 System Agent (SA) Configuration



Figure 3.37 System Agent (SA) Configuration

- **VT-d**
Enable or Disable the VT-d function.
- **Above 4GB MMIO BIOS assignment**
Enable or Disable above 4GB MemoryMappedIO BIOS assignment.
- **IPU Device (B0:D5:F0)**
Enable or Disable SA IPU device.

3.2.3.2 Memory Configuration



Figure 3.38 Memory Configuration

- **Maximum Memory Frequency**
Maximum memory frequency selections in MHz.

3.2.3.3 Graphics Configuration

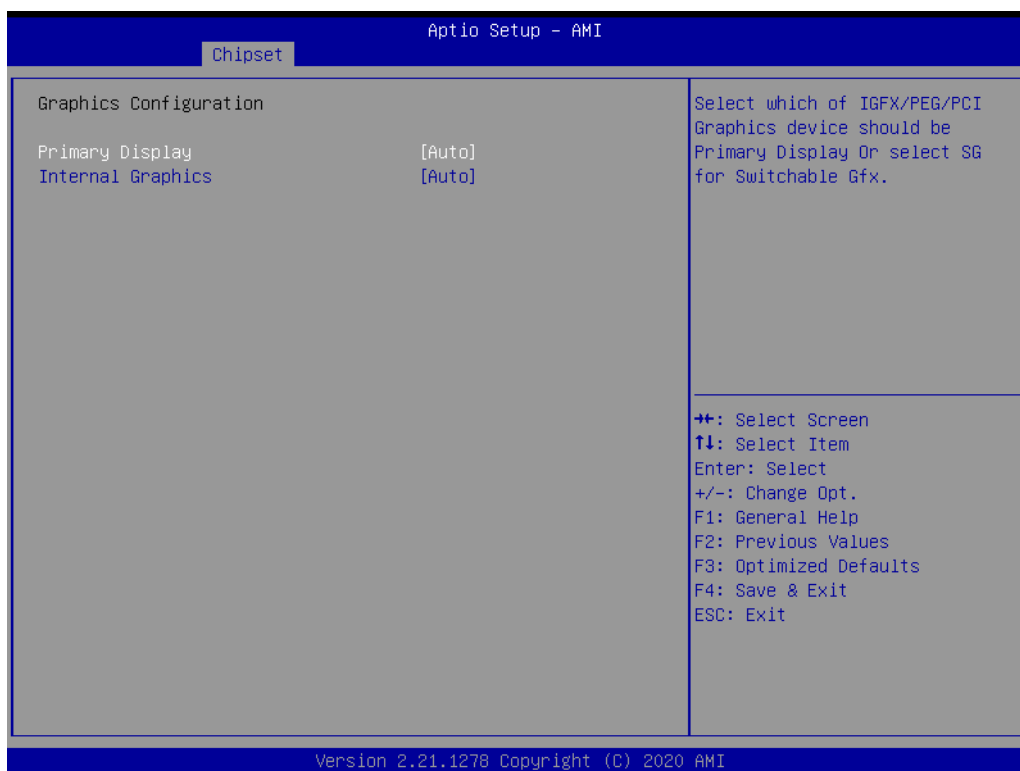


Figure 3.39 Graphics Configuration

- **Primary Display**
Set Primary Display to "Auto", "IGFX", "PEG", "PCI", or "SG".
- **Internal Graphics**
Set Internal Graphics to "Auto", "Disable", or "Enable". "Auto" will disable internal graphics when a GPU card is installed. If GPU and internal graphics outputs are required at the same time, set this item to "Enable".

3.2.3.4 PEG Port Configuration

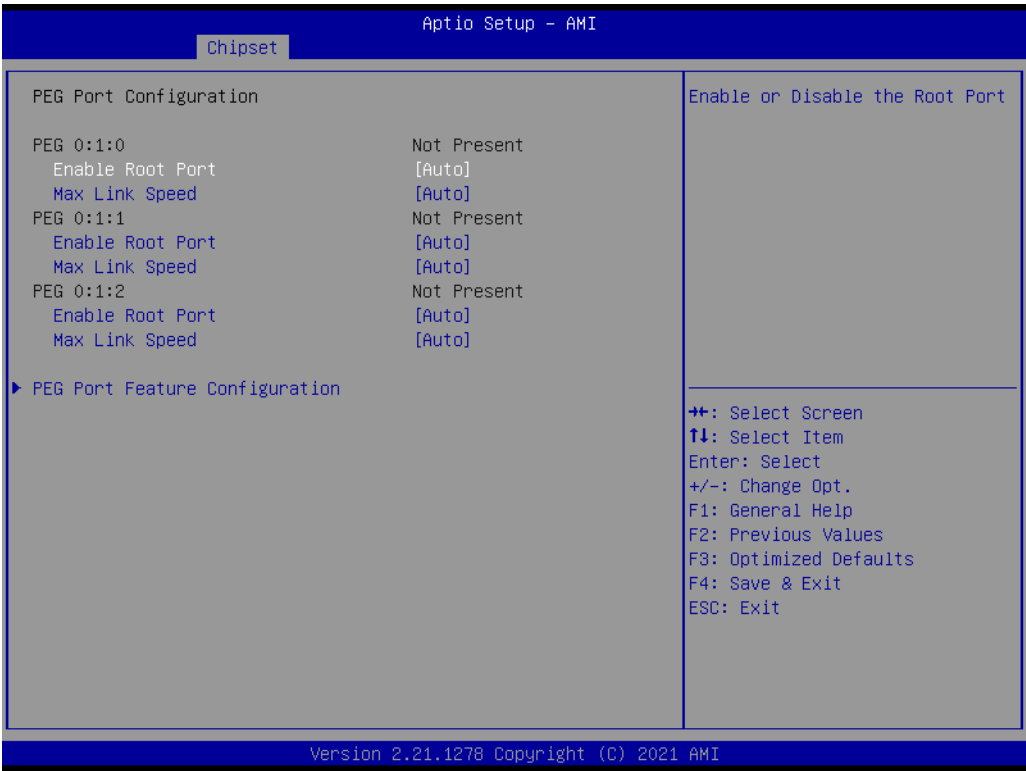


Figure 3.40 PEG Port Configuration

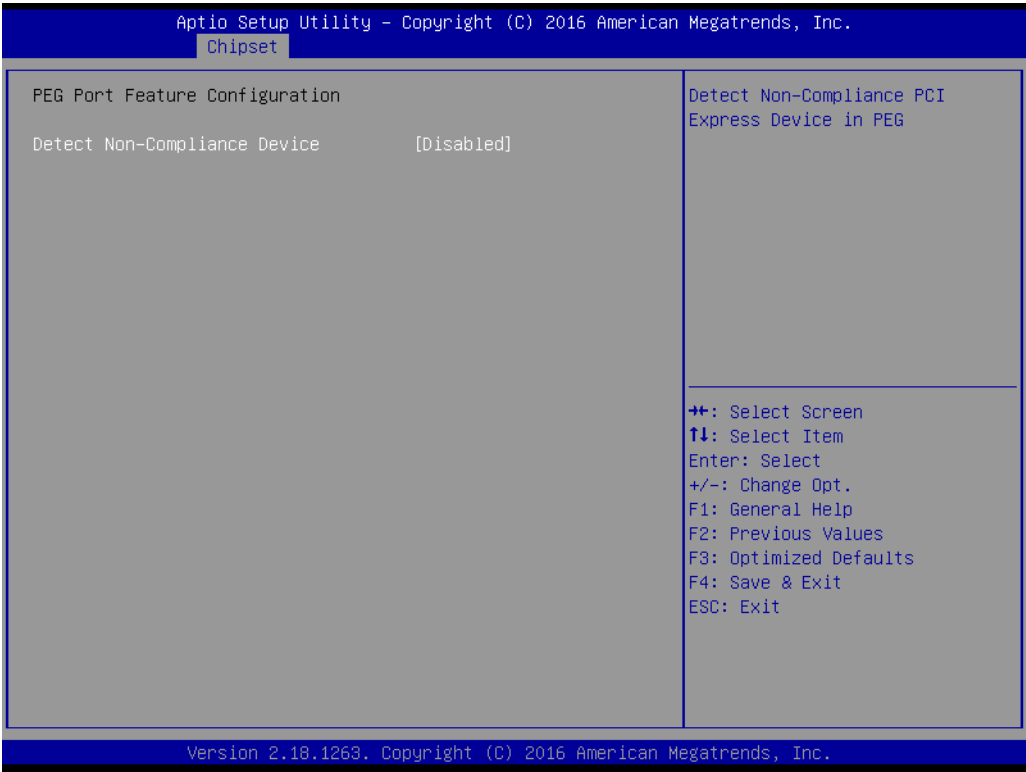


Figure 3.41 PEG Port Feature Configuration

- **Enable Root Port**
Enable or Disable the root port.
- **Max Link speed**
Configure PEG 0:1:0 max speed.

- **PEG Port Feature Configuration**
 - **Detect Non-Compliance Device**
Detect non-compliant PCI Express device in PEG. If Enabled, it will take more time during POST.

3.2.3.5 PCH-IO Configuration

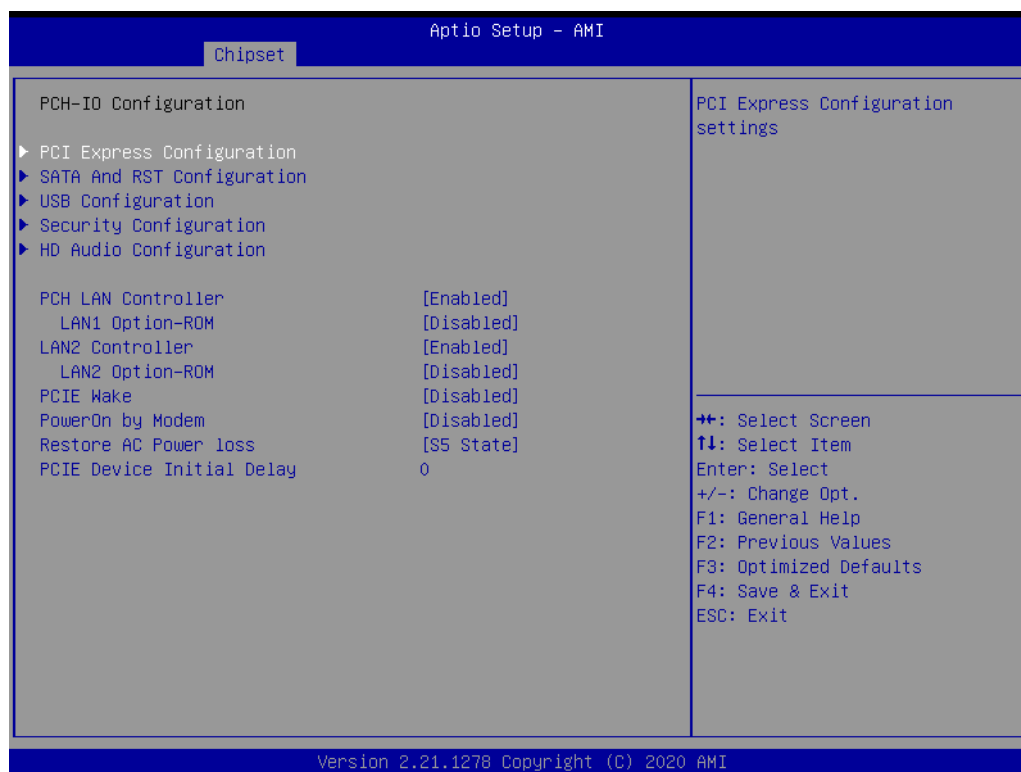


Figure 3.42 PCH-IO Configuration

- **LAN1 Controller**
Enable or Disable the LAN1 controller.
- **LAN1 Option-ROM**
Enable or Disable the LAN1 boot option for legacy network devices.
- **LAN2 Controller**
Enable or Disable the LAN2 controller.
- **LAN2 Option-ROM**
Enable or Disable the LAN2 boot option for legacy network devices.
- **PCIe Wake**
Enable or Disable PCIe to wake the system from S5. When this item is selected as Disabled, the Wake on LAN2 function is also disabled.
- **PowerOn by Modem**
Enable and Disable PowerOn by Modem
- **Restore AC Power Loss**
Behavior when recovering from AC power loss: "S0" (power on), "S5" (power off), or "Last State".
- **PCIe Device Initial Delay**
Users can set the number of seconds to delay a PCIe device initiation time.

3.2.3.6 PCI Express Configuration

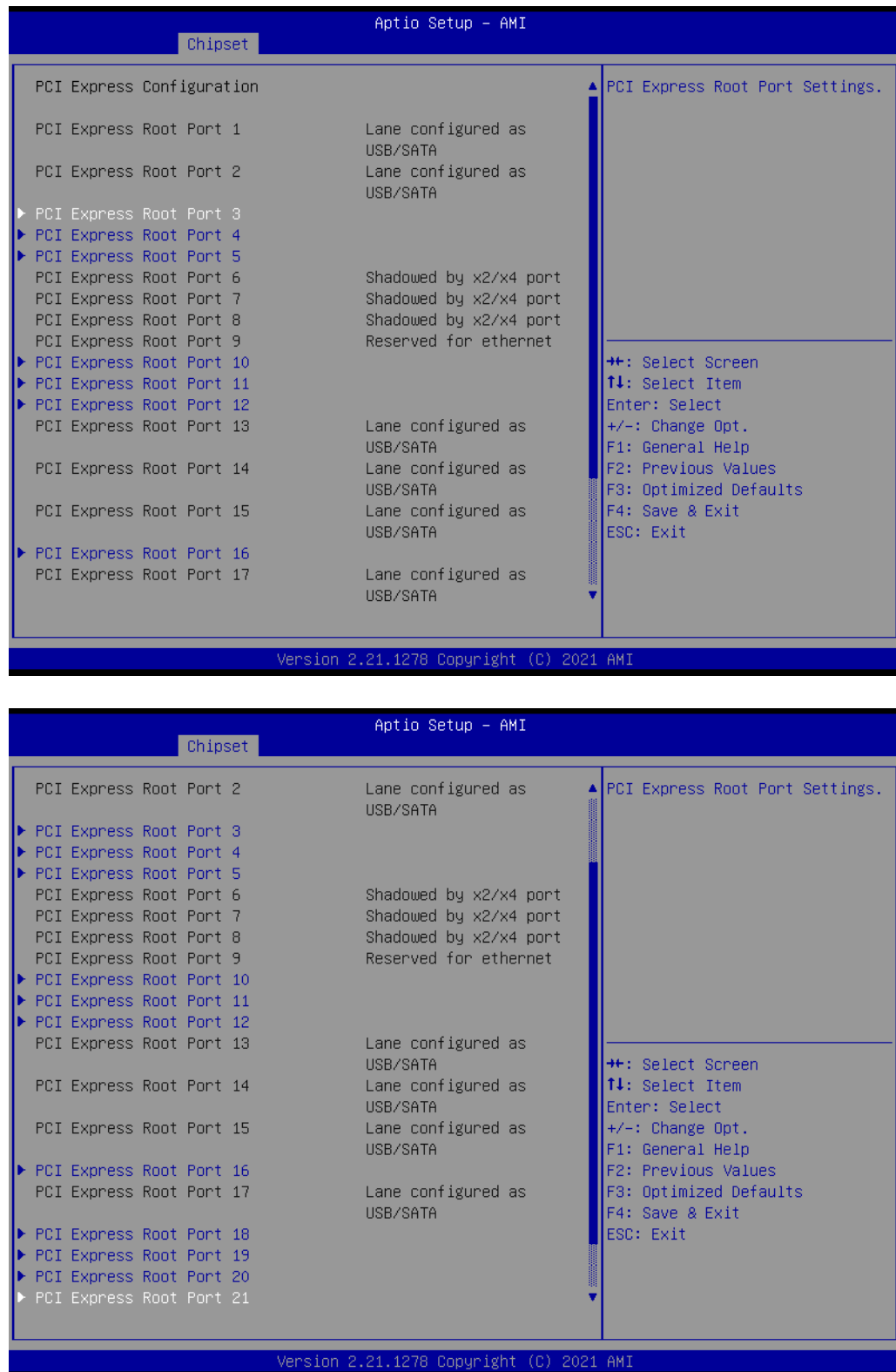


Figure 3.43 PCI Express Configuration

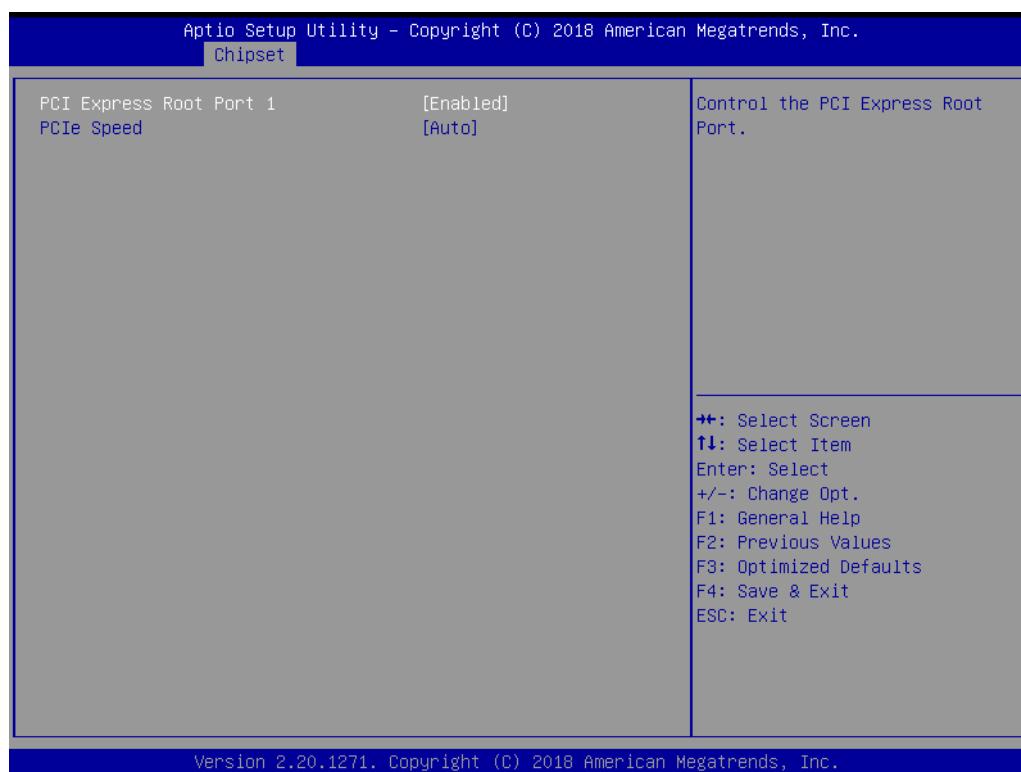


Figure 3.44 PCI Express Root Port

- **PCI Express Root Port 1**
Enable or Disable PCI Express Root Port.
- **PCIe Speed**
Select "Auto", "Gen1", "Gen2", or "Gen 3" for PCIe Speed.

3.2.3.7 SATA and RST Configuration



Figure 3.45 SATA and RST Configuration

- **SATA Controller(s)**
Enable or Disable the SATA controller.
- **SATA Mode Selection**
This can be configured as RAID or AHCI.
- **Port 1~4**
Enable or Disable SATA ports 1~4.
- **Hot Plug**
Enable or Disable SATA Hot Plug.
- **Spin Up Device**
Enable or Disable spin up device.
- **SATA Device Type**
To identify the SATA that is connected to a "Solid State Drive or Hard Disk Drive".

3.2.3.8 USB Configuration



Figure 3.46 USB Configuration

■ XHCI Compliance Mode

Option to Enable or Disable XHCI compliance mode. The default is to Disable compliance mode.

3.2.3.9 Security Configuration



Figure 3.47 Security Configuration

- **RTC Memory Lock**
"Enable" will lock bytes 38h-3Fh in the lower/upper 128-byte bank of RTC RAM.
- **BIOS Lock**
Enable or Disable the PCH BIOS Lock Enable feature. It is required to be enabled to ensure SMM protection of flash.
- **Force unlock on all GPIO pads**
If Enabled, BIOS will force all GPIO pads to be in an unlocked state.

3.2.3.10 HD Audio Configuration



Figure 3.48 HD Audio Configuration

- **HD Audio**
 Control detection of the HD-Audio device.
 Disable = HDA will be unconditionally disabled.
 Enable = HDA will be unconditionally enabled.

3.2.4 Security

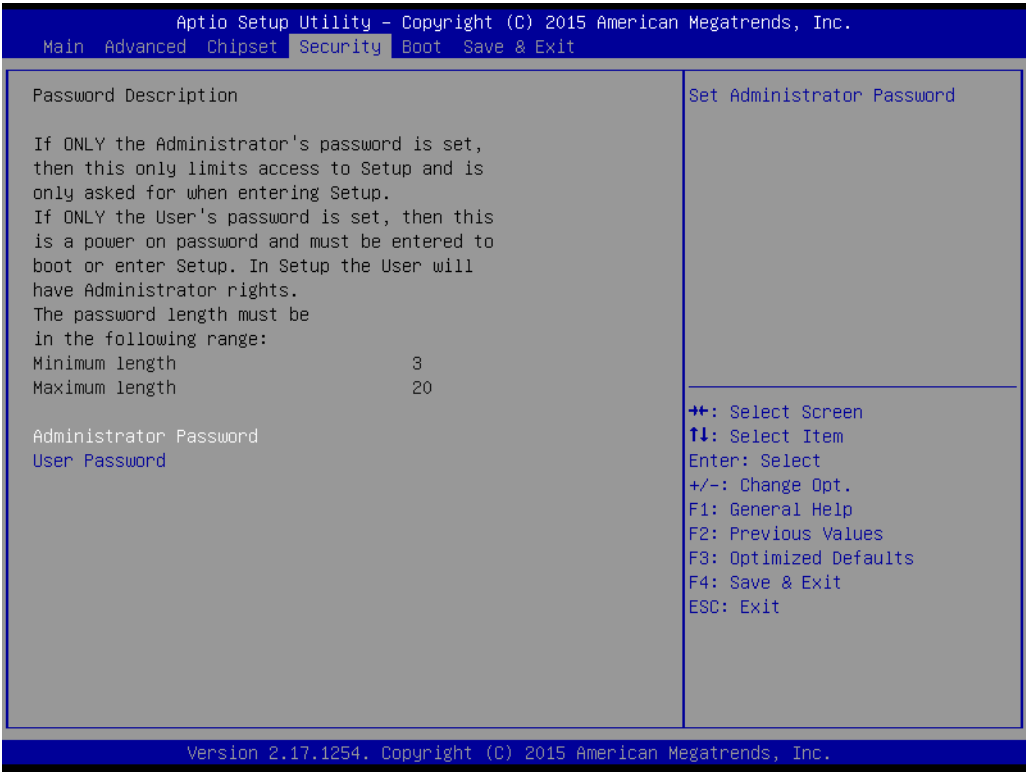


Figure 3.49 Security

Select Security Setup from the MIC-770 V2 Setup main BIOS setup menu. All Security Setup options, such as password protection are described in this section. To access the sub-menu for the following items, select the item and press <Enter>.

Note! *If only the user's password is set, the user will have administrator rights. It is strongly recommended to set an administrator password if you have security concerns.*



3.2.5 Boot

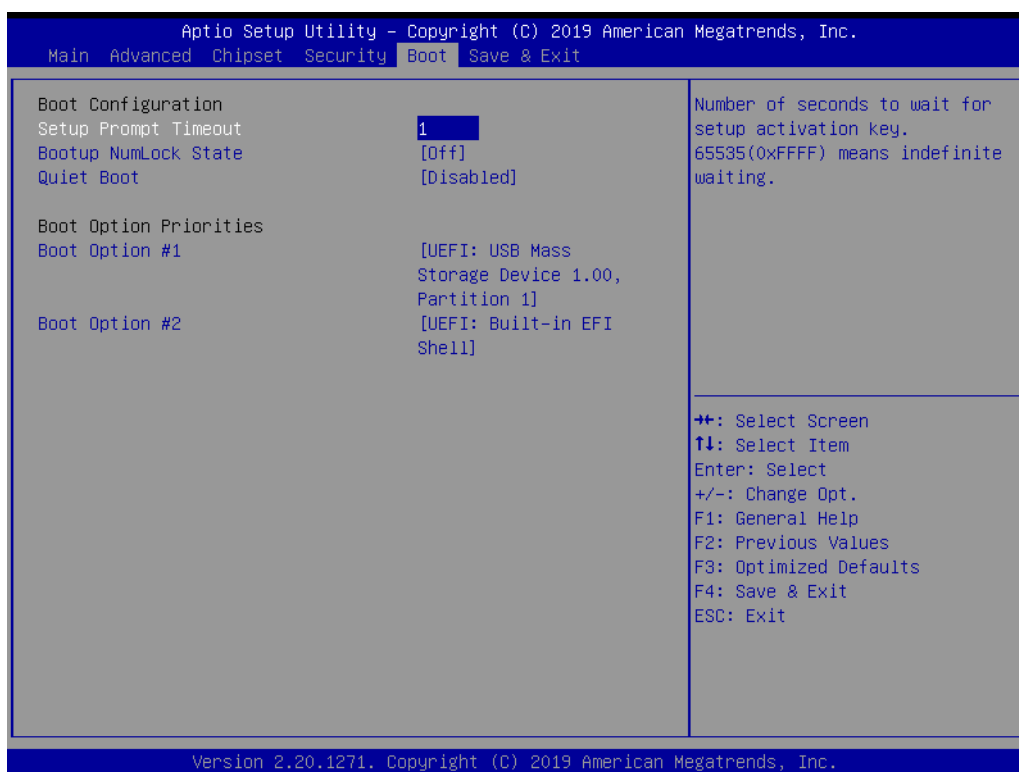


Figure 3.50 Boot

- **Setup Prompt Timeout**
Directly key in the number, or use the <+> and <-> keys to adjust the number of seconds to wait for the setup activation key.
- **Bootup NumLock State**
Default state for the NumLock key during power on.
- **Quiet Boot**
Enable or Disable the Quiet Boot option. When enabled, the BIOS logo will show in place of the POST screen.
- **Boot Option Priorities**
Set the boot order.

3.2.6 Save & Exit

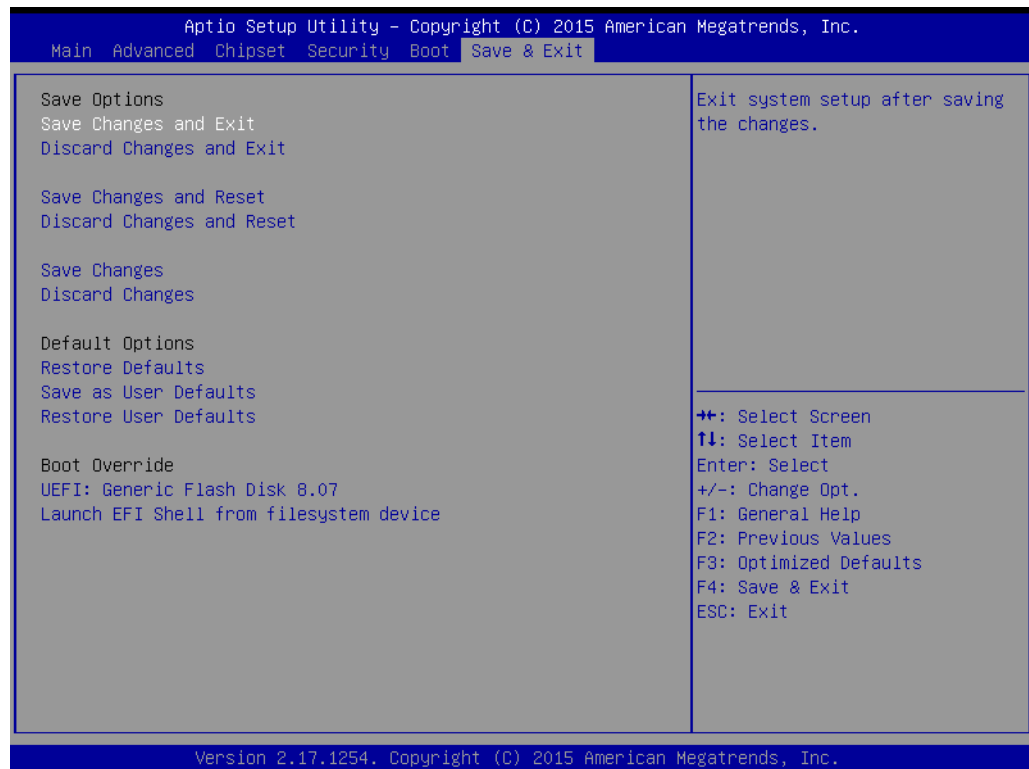


Figure 3.51 Save & Exit

Save Changes and Exit

When you complete system configuration, select this option to save your changes, exit BIOS setup and reboot the computer so the new system configuration parameters can take effect.

1. Select Exit Saving Changes from the Exit menu and press <Enter>. The following message appears:
Save Configuration Changes and Exit Now?
[Yes] [No]
2. Select Yes or No.

Discard Changes and Exit

Select this option to quit setup without making any permanent changes to the system configuration.

1. Select Exit Discarding Changes from the Exit menu and press <Enter>. The following message appears:
Quit without saving?
[Yes] [No]
2. Select Yes to discard changes and exit.

Discard Changes

Select Discard Changes from the Exit menu and press <Enter>.

Chapter 4

Software Installation

This chapter introduces driver installation.

4.1 Before You Begin

To facilitate the installation of the enhanced display drivers and utility software, read the instructions in this chapter carefully. The drivers for the MIC-770 V2 are located on the Advantech support website (<http://www.advantech.com/support>). Updates are provided via service packs from Microsoft.

Note! *For system stability, installing the drivers in the following sequence is highly recommended:*



- Chipset
- Graphics
- ME
- Other drivers

Before you begin, it is important to note that most display drivers need to have the relevant software application already installed in the system prior to installing the enhanced display drivers. In addition, many of the installation procedures assume that you are familiar with both the relevant software applications and operating system commands. Review the relevant operating system commands and the pertinent sections of your application software's user manual before performing the installation.

4.2 Introduction

The Intel® Chipset Software Installation (CSI) utility installs the Windows INF files that outline to the operating system how the chipset components will be configured. This is needed for the proper functioning of the following features:

- Core PCI PnP services
- Serial ATA interface support
- Identification of Intel chipset components in the Device Manager.

Note! *The chipset driver is used for the following versions of Windows, and it has to be installed before installing all the other drivers:*



- Windows 10 (64-bit)

4.3 Windows Driver Setup

Enter the Advantech support website, then search for the product MIC-770 V2. There you will find the MIC-770 V2 driver.

Chapter 5

Integrated Graphics Device Setup

5.1 Introduction

The Intel® processors are embedded with an integrated graphics controller. You need to install the VGA driver to enable this function, which provides the graphics, compute, media, and display capabilities.

5.2 Windows Driver Setup

Note! *Before installing this driver, make sure the INF driver has been installed in your system. See Chapter 4 for information on installing the INF driver.*



Enter the Advantech support website, then search for the product MIC-770 V2. There you will find the graphics driver.

Chapter 6

Intel® ME

6.1 Introduction

The Intel® ME software components that need to be installed depend on the system's specific hardware and firmware features. The installer detects the system's capabilities and installs the relevant drivers and applications.

6.2 Installation

Enter the Advantech support website, then search for product MIC-770 V2. There you will find the ME driver.

Chapter 7

LAN Configuration

7.1 Introduction

The MIC-770 V2 has dual Gigabit Ethernet LANs via dedicated PCI Express x1 lanes (Intel® I219LM (LAN1) and I210IT (LAN2)) that offer bandwidth of up to 500 MB/sec, eliminating the bottleneck of network data flow and incorporating Gigabit Ethernet at 1000 Mbps.

7.2 Features

- 10/100/1000Base-T Ethernet controller
- 10/100/1000Base-T triple-speed MAC
- Full duplex at 10, 100, or 1000 Mbps and half duplex at 10 or 100 Mbps
- Wake-on-LAN (WOL) support
- PCIe x1 host interface

7.3 Installation

Note! *Before installing the LAN drivers, make sure the CSI utility has been installed on your system. See Chapter 4 for information on installing the CSI utility.*



The integrated Intel® Gigabit Ethernet Controller supports all major network operating systems. However, the installation procedure varies with different operating systems. In the following sections, refer to the one that provides the driver setup procedure for the operating system you are using.

7.4 Windows Driver Setup

Enter the Advantech support website, then search for the product MIC-770 V2. There you will find the LAN driver.

Chapter 8

SATA RAID Setup

8.1 Introduction

To support the demands of disk I/O, the Intel® W480E chipset integrates four Serial ATA controllers with software and RAID 0, 1, 5, 10 capabilities.

RAID 0 striping increases storage performance and is designed to speed up data transfer rates for disk-intensive applications.

RAID 1 mirroring protects valuable data that might be lost in the event of a hard drive failure.

The RAID 5 array contains three or more hard drives where the data is divided into manageable blocks called stripes. Parity is a mathematical method for recreating data lost from a single drive, which increases fault tolerance. The data and parity are striped across all the hard drives in the array. The parity is striped in a rotating sequence to reduce bottlenecks associated with the parity calculations.

The RAID 10 array uses four hard drives to create a combination of RAID levels 0 and 1. The data is striped across a two-drive array forming the RAID 0 component. Each of the drives in the RAID 0 array is then mirrored by a RAID 1 component.

8.2 SATA RAID Driver and Utility Setup

Enter the Advantech support website, then search for product MIC-770 V2. There you will find the "Others" folder containing the RST driver.

Chapter 9

HD Audio

9.1 Introduction

MIC-770 V2 is equipped with a Realtek ALC888S Audio chip. It provides "Line-out" & "Microphone" ports for various applications.

9.2 Installation

Enter the Advantech support website, then search for product MIC-770 V2. There you will find the audio drivers.

Appendix **A**

Programming the Watchdog Timer

A.1 Programming the Watchdog Timer

The MIC-770 V2's watchdog timer can be used to monitor system software operation and take corrective action if the software fails to function within the programmed period. This section describes the operation of the watchdog timer and how to program it.

A.1.1 Watchdog Timer Overview

The watchdog timer is built into the super I/O controller NCT6106D. It provides the following user-programmable functions:

- Can be enabled and disabled by user configuration.
- The timer can be set from 1 to 255 seconds or 1 to 255 minutes.
- Generates an interrupt or reset signal if the software fails to reset the timer before time-out.

A.1.2 Programming the Watchdog Timer

The I/O port address of the watchdog timer is 2E (hex) and 2F (hex). 2E (hex) is the address port. 2F (hex) is the data port. You must first assign the address of register by writing an address value into address port 2E (hex), then write/read data to/from the assigned register through data port 2F (hex).

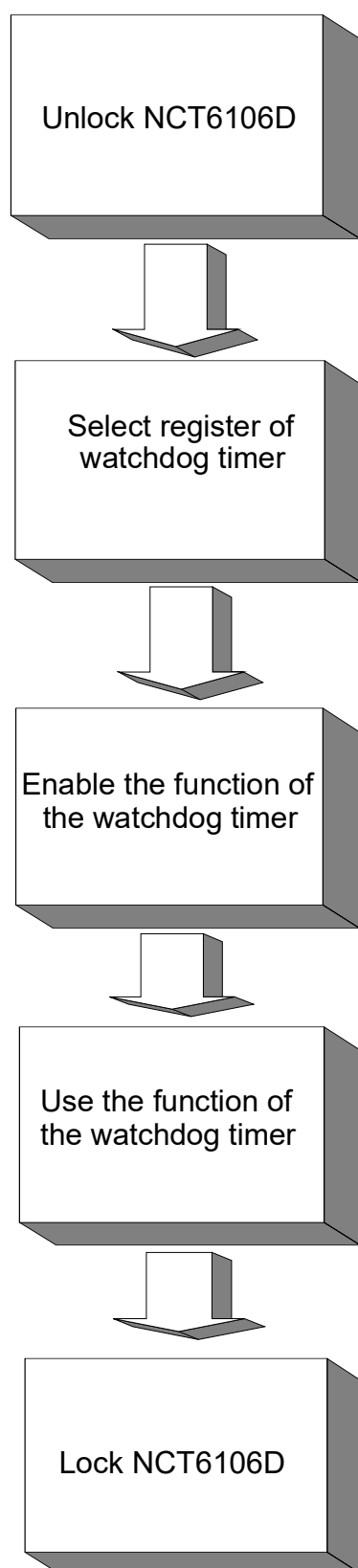


Table A.1: Watchdog Timer Register

Address of Register (2E) Attribute		
Read/Write	Value (2F) & description	
87 (hex)	-----	Write this address to I/O address port 2E (hex) twice to unlock the NCT6106D.
07 (hex)	write	Write 08 (hex) to select the register of the watchdog timer.
30 (hex)	write	Write 01 (hex) to enable the function of the watchdog timer. Disabled is set as the default.
F0 (hex)	write	Set seconds or minutes as units for the timer. Write 0 to bit 3: set seconds as the counting unit. [default] Write 1 to bit 3: set minutes as the counting unit.
F1 (hex)	write	0: stop timer [default] 01~FF (hex): The amount of the count, in seconds or minutes, depends on the value set in register F5 (hex). This number decides how long the watchdog timer waits for the strobe before generating an interrupt or reset signal. Writing a new value to this register can reset the timer to count with the new value.
F2 (hex)	read/write	Bit 7: Write 1 to enable the mouse to reset the timer, 0 to disable[default]. Bit 6: Write 1 to enable the keyboard to reset the timer, 0 to disable.[default] Bit 5: Write 1 to generate a timeout signal immediately and automatically return to 0. [default=0] Bit 4: Read status of the watchdog timer, 1 means the timer is "timeout".
AA (hex)	-----	Write this address to I/O port 2E (hex) to lock the watchdog timer 2.

A.1.3 Example Program

1. Enable the watchdog timer and set 10 sec. as the timeout interval.

```

;-----
Mov dx,2eh ; Unlock NCT6106D.
Mov al,87h
Out dx,al
Out dx,al
;-----
Mov al,07h ; Select registers of the watchdog timer.
Out dx,al
Inc dx
Mov al,08h
Out dx,al
;-----
Dec dx ; Enable the function of the watchdog timer.
Mov al,30h
Out dx,al
Inc dx
Mov al,01h
Out dx,al
;-----
Dec dx ; Set seconds as the counting unit.
Mov al,0f0h
Out dx,al
Inc dx
In al,dx
And al,not 08h
Out dx,al
;-----
Dec dx ; Set timeout interval as 10 seconds and start counting.
Mov al,0f1h
Out dx,al
Inc dx
Mov al,10
Out dx,al
;-----
Dec dx ; Lock NCT6106D
Mov al,0aah
Out dx,al

```

2. Enable the watchdog timer and set 5 minutes as the timeout interval.

```

;-----
Mov dx,2eh ; Unlock NCT6106D
Mov al,87h
Out dx,al
Out dx,al

```

```

;-----
Mov al,07h ; Select registers of the watchdog timer.
Out dx,al
Inc dx
Mov al,08h
Out dx,al
;-----
Dec dx ; Enable the function of the watchdog timer.
Mov al,30h
Out dx,al
Inc dx
Mov al,01h
Out dx,al
;-----
Dec dx ; Set minutes as the counting unit.
Mov al,0f0h
Out dx,al
Inc dx
In al,dx
Or al,08h
Out dx,al
;-----
Dec dx ; Set timeout interval as 5 minutes and start counting.
Mov al,0f1h
Out dx,al
Inc dx
Mov al,5
Out dx,al
;-----
Dec dx ; Lock NCT6106D
Mov al,0aah
Out dx,al
3. Enable the watchdog timer to be reset by mouse.
;-----
Mov dx,2eh ; Unlock NCT6106D
Mov al,87h
Out dx,al
Out dx,al
;-----
Mov al,07h ; Select registers of the watchdog timer.
Out dx,al
Inc dx
Mov al,08h
Out dx,al
;-----

```



```

Dec dx ; Enable the function of the watchdog timer.
Mov al,30h
Out dx,al
Inc dx
Mov al,01h
Out dx,al
;-----
Dec dx ; Enable the watchdog timer to be reset by mouse.
Mov al,0f2h
Out dx,al
Inc dx
In al,dx
Or al,80h
Out dx,al
;-----
Dec dx ; Lock NCT6106D
Mov al,0aah
Out dx,al
4. Enable the watchdog timer to be reset by keyboard.
;-----
Mov dx,2eh ; Unlock NCT6106D
Mov al,87h
Out dx,al
Out dx,al
;-----
Mov al,07h ; Select registers of the watchdog timer.
Out dx,al
Inc dx
Mov al,08h
Out dx,al
;-----
Dec dx ; Enable the function of the watchdog timer.
Mov al,30h
Out dx,al
Inc dx
Mov al,01h
Out dx,al
;-----
Dec dx ; Enable watchdog timer to be strobe reset by keyboard.
Mov al,0f2h
Out dx,al
Inc dx
In al,dx
Or al,40h
Out dx,al

```

```

;-----
Dec dx ; Lock NCT6106D
Mov al,0aah
Out dx,al
5.   Generate a time-out signal without the timer counting.
;-----
Mov dx,2eh ; Unlock NCT6106D.
Mov al,87h
Out dx,al
Out dx,al
;-----
Mov al,07h ; Select registers of the watchdog timer.
Out dx,al
Inc dx
Mov al,08h
Out dx,al
;-----
Dec dx ; Enable the function of the watchdog timer.
Mov al,30h
Out dx,al
Inc dx
Mov al,01h
Out dx,al
;-----
Dec dx ; Generate a time-out signal.
Mov al,0f2h
Out dx,al ;Write 1 to bit 5 of F7 register
Inc dx
In al,dx
Or al,20h
Out dx,al
;-----
Dec dx ; Lock NCT6106D.
Mov al,0aah
Out dx,al

```

Appendix **B**

Programming the GPIO

B.1 Supported GPIO Register

Below are detailed descriptions of the GPIO addresses and programming samples.

B.1.1 GPIO Registers

GPIO 1

CRF0 (GP10-GP17 I/O selection register. Default 0xFF)

When set to '1', the respective GPIO port is programmed as an input port.

When set to '0', the respective GPIO port is programmed as an output port.

CRF1 (GP10-GP17 data register. Default 0x00)

If a port is programmed to be an output port, then its respective bit can be read/written.

If a port is programmed to be an input port, then its respective bit can only be read.

CRF2 (GP10-GP17 inversion register. Default 0x00)

When set to '1', the incoming/outgoing port value is inverted.

When set to '0', the incoming/outgoing port value is the same as in the data register.

GPIO 0

CREC (GP00-GP07 I/O selection register. Default 0xFF)

When set to '1', the respective GPIO port is programmed as an input port.

When set to '0', the respective GPIO port is programmed as an output port.

CREC (GP00-GP07 data register. Default 0xFF)

If a port is programmed to be an output port, then its respective bit can be read/written.

If a port is programmed to be an input port, then its respective bit can only be read.

CREE (GP00-GP07 inversion register. Default 0x00)

When set to '1', the incoming/outgoing port value is inverted.

When set to '0', the incoming/outgoing port value is the same as in the data register.

Extended Function Index Registers (EFIRs)

The EFIRs are write-only registers with port address 2Eh or 4Eh on PC/AT systems.

Extended Function Data Registers (EFDRs)

The EFDRs are read/write registers with port address 2Fh or 4Fh on PC/AT systems.

B.1.2 GPIO Example Program

Enter the extended function mode, interruptible double-write

```
MOV DX, 2EH
MOV AL, 87H
OUT DX, AL
OUT DX, AL
```

Configure logical device 7(GP10~GP17), configuration register CRE4,CRE5,CRE6

```
MOV DX, 2EH
MOV AL, 07H ; Point to Logical Device Number Reg.
OUT DX, AL
MOV DX, 2FH
MOV AL, 07H ; Select logical device 7
OUT DX, AL
```

Configure GPIO1 I/O Register

```
MOV DX, 2EH
MOV AL, ECH
OUT DX, AL
MOV DX, 2FH
MOV AL, ??H ; 0: The respective GPIO1 PIN is programmed as an output port
               ;1: The respective GPIO1 PIN is programmed as an input port.
OUT DX, AL
```

Configure GPIO1 Inversion Register

```
MOV DX, 2EH
MOV AL, EEH
OUT DX, AL
MOV DX, 2FH
MOV AL, 00H ; Set GPIO is normal not inverter
OUT DX, AL
```

Configure GPIO1 Data Register

```
MOV DX, 2EH
MOV AL, EDH
OUT DX, AL
MOV DX, 2FH
MOV AL, ??H ; Put the output value into AL
OUT DX, AL
```

Exit extended function mode

```
MOV DX, 2EH
MOV AL, AAH
OUT DX, AL
```